

The P. O. Pistilli Undergraduate Scholarships for Advancement in Computer Science and Electrical Engineering

The objective of the P. O. Pistilli Scholarship program is to increase the pool of professionals in Electrical Engineering, Computer Engineering and Computer Science from under-represented groups (women, African American, Hispanic, Native American, and physically challenged). In 1989, ACM Special Interest Group on Design Automation (SIGDA) began providing the program. Beginning in 1993, the Design Automation Conference provided the funds for the scholarship and SIGDA continues to administer the program for DAC. DAC normally funds two or more \$4000 scholarships, renewable up to 5 years, to graduating high school seniors. In 1999 the IEEE Circuits and Systems Society also began to sponsor these scholarships.

The 2002 winners will be announced at the Conference. The 2001 winners were:

2001 DAC P. O. Pistilli Undergraduate Scholarships

Tenisha Evonne Austin - attending the University of Michigan

Sylvia Blythe Glassco - attending Yale University

Joelle B. Arnold - attending the Franklin W. Olin College of Engineering

Jieyu Hao - attending Massachusetts Institute of Technology

Mariya Sergeevna Sadakova - attending Macalester College

Todd R. Roman - attending the University of Arizona

Erika Lin - attending Massachusetts Institute of Technology

For more information about the P. O. Pistilli scholarship, please contact Dr. Cherrice Traver, EE/CS Dept., Union College, Schenectady, NY 12308. email: traverc@union.edu

Design Automation Conference Graduate Scholarships

Each year the Design Automation Conference sponsors several \$24,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems". Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Prof. **Florin Balasa**, Univ. of Illinois, Chicago, IL

Students: **Sarat Chandra Maruvada, Karthik Krishnamoorthy**

Novel Exploration Techniques in Device-Level Placement for Analog and Radio Frequency Blocks

Prof. **Dinesh K. Bhatia**, Univ. of Texas at Dallas, Richardson, TX

Students: **Shankar Balachandaran, Parivallal Kannan**

Congestion and Routability Estimation for Large ASICs

Prof. **Krishnendu Chakrabarty**, Duke Univ., Durham, NC

Student: **Lei Li**

Algorithms and Tools for Plug-and-Play System-on-a-Chip Test Automation

Prof. **Ingrid Verbauwhede**, Univ. of California, Los Angeles, CA

Student: **Patrick Schaumont**

Domain Specific Tools and Methods for Application in Security Processor Design

Information on next year's DAC scholarship award program will be available on the DAC web page: <http://www.dac.com>.