

## ***MEDIA ALERT***

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### **42<sup>nd</sup> Design Automation Conference Offers Five Full-day Tutorials Friday, June 17**

**BOULDER, Colo.** □ **June 7, 2005** □ Space is still available in the five full-day tutorials offered Friday, June 17 at the 42<sup>nd</sup> Design Automation Conference. The tutorials will provide design engineers valuable, in-depth technical training covering some of today's most important design areas. DAC, the electronic design automation (EDA) industry's premier event, will be held June 13 – 17 in Anaheim, Calif. Registration for the conference, including these tutorials, will be open until Friday, June 10, 2005 at the DAC Web site <http://www.dac.com/42nd/reg.html>. After June 10, 2005, all registration must be done at the conference.

#### **Full-Day Tutorial Scheduled for Friday, June 17, 2005:**

A tutorial on the **C-Based Design: Industrial Experience** will offer an introduction to C-based design and discuss the design techniques used in the industry by showcasing C-based design techniques in use by Philips and NEC.

The **Constraint Satisfaction Techniques for Automatic Generation of Stimuli for Functional Hardware Verification** tutorial will cover various aspects of constraint-based stimuli generation, including theoretical fields of Constraint Satisfaction Problems (CSP) and Constraint Programming (CP) and the rationale behind constraint-based stimuli generation and the methods for doing so in the context of an industrial verification platform as well as a user's viewpoint on the adaptation of these methods to real projects.

The **Advancements in Energy-Efficient Design** tutorial will address key trends in power management for energy-efficient design and the latest research on various power management techniques to address them.

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The **Design for Manufacturing at 65 nm and Below** tutorial will discuss the processes required at 65 nm and below to ensure that designs are manufacturable with acceptable yield. Attendees will learn about the problems unique to, or exaggerated by, very small devices and interconnect, such as increased process variability, increased leakage and increased electrical noise. It will then cover various techniques that can be used to mitigate these problems.

The **Design of SoC with Embedded Processors** tutorial will cover new design techniques that can help system-on-chip designers successfully tackle the design of complex multiprocessor systems-on-chips (MPSoCs). Attendees will learn about the complex software development required to meet real-time performance requirements, low power consumption levels and small program size demands of these applications.

### **About DAC**

DAC is the annual event where the electronics design community meets for a week-long forum of information exchange on management practices, products, methodologies and processes. Attended by more than 10,500 developers, designers, researchers, managers and engineers from leading electronics companies and universities worldwide, it offers a robust technical program covering the industry's hottest trends. Its vibrant exhibit floor includes more than 235 companies, many of whom are startups just introducing their first products. The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Computer Aided Network Design Technical Committee of the Institute of Electrical and Electronics Engineers (IEEE/CASS/CANDE) and the Electronic Design Automation Consortium (EDA Consortium). More details about DAC are found at: [www.dac.com](http://www.dac.com).

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