

For more information, please contact:

Emily Taylor

Weber Shandwick

503.552.3733

etaylor@webershandwick.com

Design Automation Conference Announces Executive Committee

BOULDER, Colo., Dec. 13, 2005 – The Design Automation Conference (DAC) announced its Executive Committee for the electronic design automation (EDA) industry's premier event. The committee, comprised of volunteer representatives from the electronics and EDA industries and academia, is responsible for planning the technical program, overseeing the exhibition, establishing new initiatives, managing operations and publicity for the 43rd DAC, which will be held July 24-28, 2006, at Moscone Center in San Francisco.

The 43rd DAC Executive Committee is led by General Chair, Ellen M. Sentovich of Cadence Berkeley Laboratories who will provide strategic direction for the conference. Ms. Sentovich's research at Cadence Berkeley Laboratories includes logic synthesis, system-level design, synchronous languages and design of synchronous systems. Before joining Cadence Berkeley Laboratories in 1996, she was with INRIA, the French National Computer Science Research Laboratory, where she was a postdoctoral researcher with a fellowship from the National Science Foundation and worked on synthesis from synchronous language specifications. Her experience also includes consulting work with Intel in Haifa, Israel. Ms. Sentovich received bachelor's of science, master's of science and Ph.D. degrees in electrical engineering and computer science from the University of California Berkeley. She has also served as technical chair and general chair of ICCAD, the International Conference on Computer-Aided Design.

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“This year’s Executive Committee represents a wealth of experience and expertise in the EDA and electronics communities,” said Sentovich. “Their knowledge, combined with their energy and dedication will ensure that DAC will continue to serve the industry as the most comprehensive and useful forum for designer engineers, researchers and exhibitors to learn, network and exchange information and ideas.”

Sentovich is joined by:

- Vice/Finance Chair, Steven P. Levitan, Univ. of Pittsburgh, ECE Dept., Pittsburgh, Pa.
- Technical Program Co-Chair, Design Methods, Grant E. Martin, Tensilica, Santa Clara, Calif.
- Technical Program Co-Chair, Design Tools, Sachin Sapatnekar, Univ. of Minnesota in Minneapolis, Minn.
- EDA Industry Chair, Limor Fix, Intel Research Pittsburgh, Pittsburgh, Penn.
- Past Chair, William H. Joyner, Jr., IBM Corp./SRC, Research Triangle Park, N.C.
- Tutorial Chair, Leon Stok, IBM Corp., Hopewell Junction, N.Y.
- Panel Chair, Dennis Brophy, Mentor Graphics, Wilsonville, Ore.
- Design Community Chair, Ingrid Verbauwhede, Univ. of California, Los Angeles, Calif.
- New Initiatives Chair, Andrew B. Kahng, Univ. of California at San Diego, San Diego, Calif.
- Publicity Chair, Nanette V. Collins, Nanette V. Collins Marketing and PR, Boston, Mass., and San Francisco, Calif.
- Europe/Middle East Representative, Ahmed A. Jerraya, TIMA Lab, Grenoble, France
- Asia/South Pacific Representative, Yusuke Matsunaga, Kyushu University Kagus, Fukuoka, Japan
- ACM/SIGDA Representative, Diana Marculescu, Carnegie Mellon University, Pittsburgh, Pa.
- IEEE-CASS Representative, Alan Mantooth, University of Arkansas, Fayetteville, Ark.
- EDA Consortium Representative, Anne Cirkel, Mentor Graphics, Wilsonville, Ore.

and conference management:

- Exhibits Manager, Lee Wood, MP Associates, Inc., Boulder, Colo.
- Conference Manager, Kevin Lepine, MP Associates, Inc., Boulder, Colo.

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About DAC

DAC is the premier forum for the electronic design industry to exchange information on products, methodologies and processes. Attended by more than 10,000 developers, designers, researchers, managers and engineers from leading electronics companies and universities around the world, DAC includes more than 200 exhibitors and offers a robust technical program covering the electronics industry's hottest trends to bring people to the event.

The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Computer Aided Network Design Technical Committee of the Institute of Electrical and Electronics Engineers (IEEE/CASS/CANDE), and the Electronic Design Automation Consortium (EDA Consortium). More details about DAC are found at: www.dac.com.

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