



**FOR IMMEDIATE RELEASE**

**News Release**

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**Engineering Scholarships Available for 44th Design Automation Conference**  
*Allows 15 Engineers between Jobs to Attend Premier Industry Conference*

**BOULDER, Colo.** — April 18, 2007— The Design Automation Conference (DAC), the electronic design automation (EDA) industry’s premier event, to be held June 4 – 8, 2007 at the San Diego Convention Center in San Diego, today announced it will offer the “Scholarship for DAC Alumni Between Employment” program again this year. The scholarship program was introduced in 2004 to help engineers between jobs, and therefore lacking corporate support, to attend the conference. DAC is accepting applications for these scholarships via a confidential online questionnaire on the DAC Web site until Monday, May 7. As many as 15 qualified engineers will receive the scholarships and recipients will be notified on May 21.

“This program is one of the ways in which DAC serves our community of engineers,” said Ellen Sentovich, the 2006 DAC chair, who oversees the DAC Engineering Scholarship program. “We are delighted to offer these scholarships to those who otherwise might not attend DAC this year, to give them an opportunity for the invaluable information gathering and networking that DAC provides.”

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DAC engineering scholarships, valued at approximately \$400 each, will be awarded by lottery to as many as 15 eligible applicants. To be eligible, participants must have attended DAC in the past. In addition, consideration will be given to those who have participated in other EDA industry activities. Scholarships do not include transportation and housing. In the event that a scholarship recipient's situation should change prior to the conference, he or she will be asked to give up the scholarship so another applicant may attend.

DAC offers a variety of registration options. For more details, visit the DAC Web site located at: <http://www.dac.com>

### **About DAC**

The Design Automation Conference (DAC) is the premier educational and networking event for Electronic Design Automation (EDA) and silicon solutions. More than 11,000 designers, developers, researchers, academics and managers from leading electronics companies and universities from around the world attend. DAC features close to 60 technical sessions covering the latest research on design methodologies and technologies, EDA developments and trends selected by a diverse committee of electronic design experts. A highlight is its Exhibition and Suite area with approximately 250 of the leading and emerging EDA, silicon and IP providers. More details are available at: [www.dac.com](http://www.dac.com).

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