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**News Release**

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### **44th Design Automation Conference Announces Winners of 2007 DAC/ISSCC Student Design Contest**

**BOULDER, Colo., May 1, 2007** — The Design Automation Conference (DAC), the electronic design automation (EDA) industry's premier event, has announced the winners of its annual student design contest. The awards will be presented on Monday, June 4, 2007 from noon to 1:00 p.m. in Booth 6360. Each winner will receive \$2,000.00 in prize money provided by this year's engineering community and corporate sponsors. Winning submissions also will be displayed as posters at the DAC University Booth on the exhibit floor. DAC will be held in San Diego Calif., at the San Diego Convention Center, June 4 – 8, 2007.

The nine winners, selected from nearly 50 entries, were recognized from three categories: operational chip design, for an IC design which was built and tested; operational system design, for FPGA or other programmable architectures; and conceptual, in which a project was designed and simulated, but not necessarily implemented. This year's Student Design Contest co-chairs are Bill Bowhill, Senior Principal Engineer, Intel Massachusetts; Byunghoo Jung, Assistant Professor of Electrical and Computer Engineering, Purdue University; and Alan Mantoath, Professor of Electrical Engineering, University of Arkansas.

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**2007 DAC/ISSCC Student Design Contest Winners**

*A Wireless Implantable Microsystem for Continuous Blood Glucose Monitoring* Mohammad Mahdi Ahmadi, Graham A. Jullien - Univ. Of Calgary

*Design of an Ultra-Low Voltage UWB Baseband Processor*

Vivienne Sze, Anantha P. Chandrakasan - MIT

*HBS: A Handheld Breast Cancer Detector Based on Frequency Domain Photon Migration*

Keun Sik No, Qiang Xie, Pai H. Chou - Univ. of California, Irvine

Richard Kwong, Albert Cerussi, Bruce J. Tromberg - Beckman Laser Institute

*An Energy-Efficient Reconfigurable Multiprocessor IC for DSP Applications*

Guichang Zhong, Alan N. Willson, Jr. - Univ. of California, Los Angeles

*The Scale Vector-Thread Processor*

Ronny Krashinsky, Christopher Batten, Krste Asanovic - MIT

*A 94dB SFDR 78dB DR 2.2MHz BW Multi-bit Delta-Sigma Modulator with Noise Shaping DAC*

Jianzhong Chen, Yong Ping Xu - National Univ. of Singapore

*A 230mV-to-500mV 375KHz-to-16MHz 32b RISC Core in 0.18 $\mu$ m CMOS*

Jian-Shiun Chen, Yi-Ming Wang, Yu-Juey Chang, Jinn-Shyan Wang, Tien-Fu Chen, Chingwei Yeh - Natl. Chung Cheng Univ.

*A 152mW/195mW Multimedia Processor with Fully Programmable 3D Graphics and MPEG/H.264/JPEG for Handheld Devices*

Jeong-Ho Woo, Ju-Ho Sohn, Hyejung Kim, Hoi-Jun Yoo - KAIST

Euljoo Jeong, Jongcheol Jeong, Suk Joong Lee - Corelogic, Inc.

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*A 252K gates/4.9Kbytes SRAM/71mW Multi-Standard Video Decoder for High Definition Video Applications*

Chih-Da Chien, Yi-Hung Shih, Chien-Chang Lin, He-Chun Chen, Chih-Wei Wang, Cheng-Yen Yu, Jiun-In Guo - Natl. Chung Cheng Univ.

Chih-Liang Chen, Ching-Hwa Cheng - Feng-Chia Univ.

### **Contest Criteria**

The contest accepts designs for analog, digital or programmable circuits and systems. Submissions can be embodied as integrated circuits (ICs), reconfigurable processors, systems on chips (SoCs), platform-based or embedded systems designs. The design must be part of the students' course or research work at the university and must have been completed within 18 months prior to the submission deadline.

Full-time graduate and undergraduate students interested in the 2008 Student Design Contest, should check the DAC Web site for the call for entries which will be available in the 44th DAC Proceedings in June. Past winners and more details are available online:

<http://www.dac.com/44th/studcon.html>. For more information on the Student Design Contest, contact next year's co-chairs: Bill Bowhill (bill.bowhill@intel.com) and Byunghoo Jung (jungb@purdue.edu).

### **Industry and Corporate Dedication to Continued Education**

The Student Design Contest was founded by the University of Utah's Kent Smith in 1981, and has been managed by DAC since 2000. In 2002, DAC began partnering with ISSCC to promote and manage the contest. Each year, the successful tradition of the Student Design Contest is made possible by a group of engineering community and corporate sponsors. In addition to DAC and the ISSCC, this year's engineering community sponsors include the ACM/SIGDA, the IEEE Council on EDA (CEDA), the IEEE Circuits and Systems Society and the Semiconductor Research Corporation (SRC). This year the corporate sponsors are Cadence Design Systems, Intel Corporation, Mentor Graphics Corp., Synopsys, Inc. and Tanner EDA.

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### **About DAC**

The Design Automation Conference (DAC) is the premier educational and networking event for Electronic Design Automation (EDA) and silicon solutions. More than 11,000 designers, developers, researchers, academics and managers from leading electronics companies and universities from around the world attend. DAC features close to 60 technical sessions covering the latest research on design methodologies and technologies, EDA developments and trends selected by a diverse committee of electronic design experts. A highlight is its Exhibition and Suite area with approximately 250 of the leading and emerging EDA, silicon and IP providers. More details are available at: [www.dac.com](http://www.dac.com).

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