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**Design Automation Conference Announces Executive Committee**

**BOULDER, Colo., Nov. 27, 2006** – The Design Automation Conference (DAC), the electronic design automation (EDA) industry’s premier event, today announced its Executive Committee for the 44th DAC, which will be held June 4 – 8, 2007 at the San Diego Convention Center in San Diego, Calif. The completely volunteer committee, made up of representatives from the electronics and EDA industries and academia, is responsible for planning the technical program, overseeing the exhibition, establishing new initiatives and managing operations and publicity for the conference.

Steven P. Levitan will serve as general chair and provide direction for the 44<sup>th</sup> DAC Executive Committee. Levitan is the John A. Jurenko Professor of Computer Engineering in the Department of Electrical and Computer Engineering at the University of Pittsburgh with a joint appointment in the Department of Computer Science. His research at the University of Pittsburgh includes the design, modeling, simulation and verification of mixed technology micro-systems, including sensing, computing and communications functions.

Levitan received a bachelor of science degree in Computer Science and Electrical Engineering from Case Western University and master of science and Ph.D. degrees in Computer Science from the University of Massachusetts, Amherst.

He has also served as chair of ACM Special Interest Group on Design Automation (SIGDA) and has been a member of the Executive Committee of the Design Automation

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Conference since 1998. Levitan was also awarded the Best Paper at the 34th IEEE/ACM Design Automation Conference, in Design Methodology. He received the ACM/SIGDA Service Award in appreciation for Contributions to ACM/SIGDA in 2005 and the ACM/SIGDA Distinguished Service Award recognizing over a decade of service to ACM/SIGDA and the EDA industry in 2002.

“This year’s Executive Committee is already hard at work ensuring that the 44<sup>th</sup> DAC will provide invaluable learning and networking opportunities for the global electronic design community,” said Levitan. “With the combined energy, expertise and experience of this remarkable group of volunteers driving it, we are looking forward to a very strong conference in San Diego next June.”

Dr. Levitan is joined by:

- Vice/Finance Chair, Limor Fix, Intel Research Pittsburgh, Pittsburgh, Pa.
- Technical Program Co-Chair, Design Methods, Leon Stok, IBM Corp., Hopewell Junction, N.Y.
- Technical Program Co-Chair, Design Tools, Sachin Sapatnekar, University of Minnesota, Minneapolis, Minn.
- EDA Industry Chair, Yervant Zorian, Virage Logic Corp., Fremont, Calif.
- Past Chair, Ellen M. Sentovich, Cadence Berkeley Labs, Berkeley, Calif.
- Tutorial Chair, Narendra Shenoy, Synopsys, Inc., Mountain View, Calif.
- Panel Chair, Andrew B. Kahng, University of California at San Diego, San Diego, Calif.
- Design Community Chair, Kaushik Roy, Purdue University, West Lafayette, Ind.
- New Initiatives Chair, Dennis Brophy, Mentor Graphics, Wilsonville, Ore.
- Publicity Chair, Nanette V. Collins, Nanette V. Collins Marketing and PR, Boston, Mass. and San Francisco, Calif.
- Europe/Middle East Representative, Georges Gielen, Katholieke University, Leuven, Belgium
- Asia/South Pacific Representative, Yusuke Matsunaga, Kyushu University Kagus, Fukuoka, Japan
- ACM/SIGDA Representative, Diana Marculescu, Carnegie Mellon University, Pittsburgh, Pa.
- IEEE-CASS Representative, Alan Mantooh, University of Arkansas, Fayetteville, Ark.
- EDA Consortium Representative, Anne Cirkel, Mentor Graphics, Wilsonville, Ore.

And conference management:

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- Exhibits Manager, Lee Wood, MP Associates, Inc., Boulder, Colo.
- Conference Manager, Kevin Lepine, MP Associates, Inc., Boulder, Colo.

### **About DAC**

The Design Automation Conference (DAC) is the premier educational and networking event for Electronic Design Automation (EDA) and silicon solutions. More than 11,000 designers, developers, researchers, academics and managers from leading electronics companies and universities from around the world attend. DAC features close to 60 technical sessions covering the latest research on design methodologies and technologies, EDA tool developments and trends selected by a diverse committee of electronic design experts. A highlight is its Exhibition and Suite area with approximately 250 of the leading and emerging EDA, silicon and IP providers. More details about DAC are available at: [www.dac.com](http://www.dac.com).

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