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Industry Sponsorship Support Encouraged for DAC Student Design Contest
Submissions Accepted Until December 6

BOULDER, Colo. - Nov. 30, 2006 – Today the Design Automation Conference (DAC) announced that industry sponsorships are being sought for this year’s Student Design Contest. The prestigious annual contest, jointly sponsored by the Design Automation Conference (DAC) and the International Solid State Circuits Conference (ISSCC), promotes excellence in electronic systems design and is made possible through the contributions of corporate sponsors. Student Design Contest entries are being accepted through Dec. 6, 2006. Winners will be recognized in an award ceremony to be held during DAC, the electronic design automation (EDA) industry’s premier event, June 4 – 8, 2007 at the San Diego Convention Center in San Diego.

This year’s Student Design Contest co-chairs are Bill Bowhill, Senior Principal Engineer, Intel Massachusetts; Byunghoo Jung, Assistant Professor of Electrical and Computer Engineering, Purdue University; and Alan Mantooh, Professor of Electrical Engineering, University of Arkansas.

“The DAC/ISSCC Student Design Contest is a great opportunity for graduate and undergraduate students at universities and colleges worldwide to present their research work,” said Jung. “And it also gives the sponsor company representatives who serve as judges the chance to see firsthand the work of the next generation of electronic designers.”

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Criteria for Entering, Awards and Honors

The contest accepts designs for analog, digital or programmable circuits and systems. Submissions can be embodied as integrated circuits (ICs), reconfigurable processors, systems on chips (SoCs), platform-based or embedded systems designs.

Submissions are invited from full-time graduate and undergraduate students in three categories: operational, which means that an IC design was built and tested; system design, which focuses on FPGA or other programmable architectures; and conceptual, where a project was designed and simulated, but need not have been implemented yet. The design must be part of the students' course or research work at the university and must have been completed within 18 months prior to the Dec. 6, 2006 submission deadline.

The total prize money is expected to be more than \$20,000, shared among 10 design award recipients. Winners will be notified prior to the 44th DAC and offered travel assistance to attend.

Winning submissions will be displayed as posters at the DAC University Booth on the exhibit floor. Selected winning entries may be included in the Technical Program, at the discretion of the Technical Program Committee. Winners will also be invited to present at a special poster session at ISSCC 2007 to be held in February in San Francisco.

Corporate Sponsorships Available

Byunghoo Jung is actively soliciting electronics companies for \$2,000 contributions to support the contest. As a sponsor, company representatives serve as judges and get first access to all design projects.

Last year's corporate sponsors included Cadence Design Systems, IBM Corp., Intel Corp., Mentor Graphics, Mindspeed Technologies, Inc., Synopsys, Inc. and Tanner EDA, along with industry support from the IEEE Council on EDA, the IEEE Circuits and Systems Society, and the Semiconductor Research Corporation (SRC).

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Student Design Contest, Sponsorship Opportunity Details

For more details on the Student Design Contest, visit the DAC Web site: <http://www.dac.com/44th/studcon.html>. To find out more about sponsorship opportunities, contact Byunghoo Jung at (765) 494-2866 or via e-mail at jungb@purdue.edu.

The Student Design Contest was founded by the University of Utah's Kent Smith in 1981, and has been managed by DAC since 2000. In 2002, DAC began partnering with ISSCC to promote and manage the contest.

About DAC

The Design Automation Conference (DAC) is the premier educational and networking event for Electronic Design Automation (EDA) and silicon solutions. More than 11,000 designers, developers, researchers, academics and managers from leading electronics companies and universities from around the world attend. DAC features close to 60 technical sessions covering the latest research on design methodologies and technologies, EDA tool developments and trends selected by a diverse committee of electronic design experts. A highlight is its Exhibition and Suite area with approximately 250 of the leading and emerging EDA, silicon and IP providers. More details about DAC are available at: www.dac.com.

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