

43RD DAC DEMOGRAPHICS



Conference, Exhibit-only, Free Monday: 6875

- Conference: 3173
- Exhibit-only: 1015
- Free-Monday: 2689

• USA = 81%

• International = 19%

Exhibitors, Visitors and Guests: 5077

Total Participants: 11,952

43rd DAC Registration Questionnaire Results:

1) What is your principal job function?

(101) Exec./Senior Mgmt.	16.43%
(102) Design Eng. Mgmt.	11.66%
(103) EDA Dev. Eng. Mgmt.	9.67%
(104) Design Engineer	12.18%
(105) EDA Dev. Engineer	12.38%
(106) Systems Design Engineer	1.57%
(107) Verification Engineer	4.14%
(108) System Architect	1.65%
(109) Physical Design Engineer	6.75%
(110) Embedded Software Designer	0.80%
(111) Test Engineer	1.24%
(112) Research/Professor	6.12%
(113) Marketing Sales	6.80%
(114) Press/Analyst	2.08%
(115) Student	6.52%

2) What type of design are you involved in?

(201) Analog ICs/Mixed Signal ICs	18.52%
(202) Analog/Mixed Signal	14.78%
(203) ASIC	40.75%
(204) Application Specific Std. Part (ASSP)	8.03%
(205) Digital ICs	28.77%
(206) Digital Systems	14.75%
(207) DSPs	8.72%
(208) Embedded Systems	13.93%
(209) MEMS	2.38%
(210) Memory	9.63%
(211) Microproc./Microcontroller Design	11.16%
(212) PCB Layout	5.55%
(213) PLDs/FPGAs	9.85%
(214) R/F Microwave	4.73%
(215) System Design Architecture	12.91%
(216) System Design/Integration	11.05%
(217) Library Models/IP/Cores	10.80%
(218) Not Directly Involved in Design	18.97%

3) What primary end product or service do you work on?

(301) Automotive Electronics	2.09%
(302) Avionics, Military, Government	2.36%
(303) Communication Systems (Wired or Wireless)	10.25%
(304) Components	3.30%
(305) Computers, Systems, & Peripherals	8.20%
(306) Consumer Electronics	7.33%
(307) Control, Test, Medical Equipment	1.56%
(308) Design Services/Consultant	5.33%
(309) EDA Software	26.84%
(310) Embedded Systems	3.18%
(311) Research/Education	6.58%
(312) Semiconductors	17.71%
(313) Library Models/IP/Cores	2.39%
(314) Media/Publishing	2.86%

4) Which products and/or services in the following areas do you recommend, purchase or influence the purchase of?

Design Tools/Services

(401) Analog/AMS Design/Simulation	19.62%
(402) Digital Simulation	28.07%
(403) Embedded Systems	13.33%
(404) Logic Design	26.47%
(405) Packaging	7.32%
(406) PCB	7.24%
(407) Placement & Routing	20.76%
(408) Full-Custom Layout	14.02%
(409) Test	12.73%
(410) RTL & Formal Verification	24.40%
(411) Equivalence Checking	14.86%
(412) System Design	18.39%

Services

(413) Logic/System Design	17.52%
(414) Physical Design	24.38%
(415) Verification	24.02%

Other

(416) Libraries/Models	17.24%
(417) IP/Cores	20.00%
(418) None	24.64%

5) Which tool flow do you predominately use?

(501) Custom/Structured Custom	28.60%
(502) COT	12.16%
(503) ASIC	42.27%
(504) Structured ASIC	3.57%
(505) FPGA	13.40%

7) What is your company's design budget in the next 12 months for tools and IP (incl. Cores and Libraries) only?

(701) None	16.46%
(702) Less than \$100K	8.15%
(703) \$100K to \$999K	10.57%
(704) \$1M or Greater	19.83%
(705) Not Sure	44.99%

8) What is your Gender?

(801) Male	86.64%
(802) Female	13.36%

9) What is your age range?

(901) <35	32.47%
(902) 35-55	61.72%
(903) >55	5.81%