

DAC Advance Registration Form 44th DAC - June 4-8, 2007

Any registration received after May 7, 2007, will be charged the at-conference rate. If payment is received from a non-US bank, attendees will be charged a collection fee of \$30.00.

1 ATTENDEE INFORMATION

First Name _____ Last Name _____ Title _____
 Company _____ Address _____
 City _____ State/Province _____ ZIP _____ Country _____
 Phone _____ Fax _____ Email (needed for receipt) _____

2 MEMBERSHIP STATUS

Membership # must be included at time of submission to receive the membership rate. No refunds will be made for change in membership status.

I certify that I am an ACM member. _____ member# _____ This is for **individual memberships only**. This does not apply to company memberships.
signature

I certify that I am an IEEE member. _____ member# _____
signature

I certify that I am a full time student. _____ student# _____
signature

3 TUTORIAL/WORKSHOP SELECTION

PRICES

<u>member ACM/IEEE</u>	<u>non-member</u>	<u>ACM/IEEE student</u>
\$300.00	\$375.00	\$125.00

Monday Tutorials

1) Anatomy of Variability and Making of "Variation Tolerance" Vaccine in Nanometer Technologies

2) System Design for Multimedia Applications – Challenges, Design Methods and Recent Developments

Friday Tutorials

3) Formal Assertion Based Verification in an Industrial Setting

4) Die and Package Power Delivery Analysis for High Performance and Low Power Systems

5) Soft Errors: Technology Trends, System Effects and Design Techniques

6) How Design Meets Yield in the Fab

7) Circuit and CAD Techniques for Low Power Design

Sunday Workshop

4th UML for SoC Design **\$100.00 member \$150.00 non-member**

Low Power Coalition Workshop—Standards for Low Power Design Intent **\$75.00 member \$100.00 non-member**

Design and Verification of Low Power ICs **\$75.00 member \$100.00 non-member**

Hardware Dependent Software (HDS) **\$100.00 member \$150.00 non-member**

Monday Workshops

3rd Integrated Design Systems Workshop: *Models for Design & Manufacturing - How Modeling Challenges are Touching Every Aspect of IC Design*

Introduction to Chips and EDA for a Non-Technical Audience - **\$10.00**

Tuesday

Management Seminar - **\$350.00**

Hands-on Tutorials \$75 each

A) Standard Cell Library and Hard IP Design, *Blaze DFM, Inc., Ponte Solutions* (MONDAY AM)

B) Design for Manufacturing Variability with Confidence, *ClearShape Technologies, Cadence Design Systems, Inc., Nannor Technologies, Inc.* (MONDAY PM)

C) Deploying Statistical Timing – from Characterization to Analysis and Optimization, *Altos Design Automation, Cadence Design Systems, Inc.* (TUESDAY PM)

D) Approaching Yield in the Nanometer Age the Framework for an Extensible DFM Methodology, *Mentor Graphics Corp., Chartered Semiconductor Manufacturing, Sierra Design Automation, Inc., ARM* (WEDNESDAY AM)

E) Manufacturing Aware Optimization, *Blaze DFM, Inc., Taiwan Semiconductor Manufacturing Company, Ltd.* (WEDNESDAY PM)

F) Timing Closure: Requirements for Variation Aware Design, *ExtremeDA, Corp.* (THURSDAY AM)

G) CMP Issues, *XYALIS* (THURSDAY PM)

Please refer to the DAC web site for restrictions regarding conflicts of interest.

4 REGISTRATION OPTIONS

<u>CONFERENCE ONLY</u>	Received by May 7, 2007	After May 7, 2007 or At-Conference
Member ACM/IEEE	\$380.00	\$475.00
Non-member	\$495.00	\$620.00
ACM/IEEE Students	\$165.00	\$165.00
Exhibit-Only	\$65.00	\$65.00
One-Day Only (Tues/Wed/Thur)	\$255.00	\$255.00
Two-Day Only (Tues/Wed/Thur)	\$455.00	\$455.00
<i>please circle day(s)</i>		
Workshop Only (member/ non-member)	<i>please select workshop</i>	
<input type="checkbox"/> vegetarian meal		
Conference Fees	\$ _____	
Tutorial Fees	\$ _____	
Hands-On Tutorial Fees	\$ _____	
Workshop Fees	\$ _____	
Proceedings-DVD (\$50)	\$ _____	
Management Seminar (\$350)	\$ _____	
Addl. Guest/Party Ticket (\$80)	\$ _____	
TOTAL COST	\$ _____	

5 PAYMENT INFORMATION

Credit Cards:

VISA

MASTERCARD

AMEX.

Name _____ Exp. Date _____
Please print name as it appears on the credit card

Card Number _____

Signature _____

I agree to pay the total amount according to the card issuer agreement.

6 ATTENDEE SURVEY

1) In which industry segment does your group or organization work?

<input type="checkbox"/> (101) System Design - System	<input type="checkbox"/> (220) Editor/Publisher	<input type="checkbox"/> (406) Consumer Electronics
<input type="checkbox"/> (102) System Design - Board	<input type="checkbox"/> (221) Industry Analyst	<input type="checkbox"/> (407) Control, Test, Medical Equipment
<input type="checkbox"/> (103) System Design - Hardware	<input type="checkbox"/> (222) Marketing/Sales	<input type="checkbox"/> (408) Design Services/Consultant
<input type="checkbox"/> (104) System Design - Software	<input type="checkbox"/> (223) Other _____	<input type="checkbox"/> (409) EDA Software
<input type="checkbox"/> (105) EDA Software		<input type="checkbox"/> (410) Embedded Systems
<input type="checkbox"/> (106) IP/Core Design		<input type="checkbox"/> (411) Research/Education
<input type="checkbox"/> (107) Foundry/IC Manufacturing		<input type="checkbox"/> (412) Semiconductors
<input type="checkbox"/> (108) Design Services		<input type="checkbox"/> (413) Library Models/IP/Cores
<input type="checkbox"/> (109) University/Research Institution		<input type="checkbox"/> (414) Media/Publishing
<input type="checkbox"/> (110) Financial		<input type="checkbox"/> (415) Other _____
<input type="checkbox"/> (111) Publishing/Media/Industry Analysis		
<input type="checkbox"/> (112) Other _____		

2) What is your principal job function?

<input type="checkbox"/> (201) Executive/Senior Management	<input type="checkbox"/> (301) Analog ICs/Mixed Signal ICs	<input type="checkbox"/> (501) Analog/AMS Design/Simulation
<input type="checkbox"/> (202) Engineering Management	<input type="checkbox"/> (302) Analog/Mixed Signal Systems	<input type="checkbox"/> (502) Digital Logic Simulation
<input type="checkbox"/> (203) CAD Engineering Management	<input type="checkbox"/> (303) ASIC	<input type="checkbox"/> (503) Physical Verification
<input type="checkbox"/> (204) CAD Engineer	<input type="checkbox"/> (304) Application Specific Std. Part (ASSP)	<input type="checkbox"/> (504) Other Logic Verification
<input type="checkbox"/> (205) Hardware Logic Design Engineer	<input type="checkbox"/> (305) Digital ICs	<input type="checkbox"/> (505) Logic Design and Synthesis
<input type="checkbox"/> (206) Hardware Physical Design Engineer	<input type="checkbox"/> (306) Digital Systems	<input type="checkbox"/> (506) Packaging
<input type="checkbox"/> (207) Verification Engineer	<input type="checkbox"/> (307) DSPs	<input type="checkbox"/> (507) PCB
<input type="checkbox"/> (208) Application Engineer	<input type="checkbox"/> (308) RTOS	<input type="checkbox"/> (508) Placement & Routing
<input type="checkbox"/> (209) Embedded Software Engineer	<input type="checkbox"/> (309) Firmware	<input type="checkbox"/> (509) Full-Custom Layout
<input type="checkbox"/> (210) Application Software Engineer	<input type="checkbox"/> (310) Middleware	<input type="checkbox"/> (510) Test
<input type="checkbox"/> (211) Board Design	<input type="checkbox"/> (311) Application	<input type="checkbox"/> (511) RTL & Formal Verification
<input type="checkbox"/> (212) Board Layout	<input type="checkbox"/> (312) Embedded Systems	<input type="checkbox"/> (512) Equivalence Checking
<input type="checkbox"/> (213) System Architect	<input type="checkbox"/> (313) MEMS	<input type="checkbox"/> (513) Design for Manufacturing
<input type="checkbox"/> (214) Professor	<input type="checkbox"/> (314) Memory	<input type="checkbox"/> (514) Algorithm Design
<input type="checkbox"/> (215) Researcher	<input type="checkbox"/> (315) Microproc./Microcontroller Design	<input type="checkbox"/> (515) System Design and Specification
<input type="checkbox"/> (216) Student	<input type="checkbox"/> (316) PCB Layout	<input type="checkbox"/> (516) Software Design
<input type="checkbox"/> (217) Engineering IT Management	<input type="checkbox"/> (317) PLDs/FPGAs	<input type="checkbox"/> (517) HW/SW co-Design
<input type="checkbox"/> (218) Financial Analyst	<input type="checkbox"/> (318) R/F Microwave	<input type="checkbox"/> (518) Foundry/Silicon Vendor
<input type="checkbox"/> (219) Venture Capitalist	<input type="checkbox"/> (319) Specialized Processors (GPU, NPU, etc.)	<input type="checkbox"/> (519) ASSP
	<input type="checkbox"/> (320) Library Models/IP/Cores	
	<input type="checkbox"/> (321) Not Directly Involved in Design	
	<input type="checkbox"/> (322) Other _____	

3) What type of design are you involved in?

4) What primary end product or service do you work on?

5) Which products and/or services in the following areas do you recommend, purchase or influence the purchase of? (Answer all that apply)

Design Tools/Services

6) Which tool flow do you predominately use? (Answer only one)

7) Which hotel will you be using while in San Diego?

8) Is this the first time you have attended DAC?

Services

Other

DAC provides an email list of attendees to exhibitors. If you do not want to receive this correspondence check here. _____

7 SUBMIT VIA FAX OR MAIL TO:

Make checks payable to:
44th Design Automation Conference.

44th Design Automation Conference
 Attn: Registration Desk
 5405 Spine Rd., Ste. 102
 Boulder, CO 80301 USA

Fax registrations accepted with credit card payment only!
Phone Number: (303) 530-4333
Toll-Free: (800) 321-4573
Fax Number: (303) 530-4334

Refund Policy: Written requests for cancellations must be received on or before May 7, 2007, and are subject to a \$25.00 processing fee. Cancellations after May 7, 2007, will not be honored and all registration fees will be forfeited. No mail or fax registrations will be accepted after May 25, 2007 in the DAC office. On-line registrations will be accepted through June 8, 2007.

TELEPHONE REGISTRATIONS ARE NOT ACCEPTED! Fax registrations without payments will be discarded.