

45TH DAC DEMOGRAPHICS



DESIGN AUTOMATION CONFERENCE

Conference 2382
Exhibit-Only 1842
Total 4223
Exhibitors, Visitors and Guests: 3543
Total Participants 7766

45th DAC Registration Questionnaire Results:

1) In which industry segment does your group or organization work? (answer only one)

(101) System Design - System.....	10.43%
(102) System Design - Board.....	1.43%
(103) System Design - Hardware.....	18.23%
(104) System Design - Software.....	3.32%
(105) EDA Software.....	23.77%
(106) IP/Core Design.....	12.49%
(107) Foundry/IC Manufacturing.....	5.93%
(108) Design Services.....	6.88%
(109) University/Research Institution.....	12.79%
(110) Financial.....	1.33%
(111) Publishing/Media/Industry Analysis.....	3.41%
(112) Other.....	1.69%

2) What is your principal job function (answer only one)

(201) Executive/Senior Mgmt.....	17.26%
(202) Engineering Management.....	14.52%
(203) CAD Engineering Management.....	6.15%
(204) CAD Engineer.....	9.27%
(205) Hardware Logic Design Engineer.....	7.01%
(206) Hardware Physical Design Engineer.....	5.06%
(207) Verification Engineer.....	3.52%
(208) Application Engineer.....	2.35%
(209) Embedded Software Engineer.....	0.80%
(210) Application Software Engineer.....	1.59%
(211) Board Design & Layout.....	0.50%
(212) Test & Assembly.....	0.44%
(213) System Architect.....	1.74%
(214) Professor/Researcher.....	9.13%
(215) Student.....	8.35%
(216) Financial or Industry Analyst.....	0.80%
(217) Venture Capitalist.....	0.42%
(218) Editor/Publisher.....	1.74%
(219) Marketing/Sales.....	7.15%
(220) Other.....	2.20%

3) What type of design are you involved in? (answer all that apply)

(301) Analog ICs/Mixed Signal ICs.....	24.29%
(302) Analog/Mixed Signal Systems.....	13.58%
(303) ASIC.....	40.37%
(304) Application Specific Std. Part (ASSP).....	8.70%
(305) Digital ICs.....	32.30%
(306) Digital Systems.....	18.06%
(307) DSPs.....	8.42%
(308) RTOS.....	3.64%
(309) Firmware.....	6.64%
(310) Middleware.....	2.89%
(311) Application.....	7.47%
(312) Embedded Systems.....	17.78%
(313) MEMs.....	2.98%
(314) Memory.....	10.13%
(315) Microproc./Microcontroller Design.....	10.45%
(316) PCB Layout.....	6.34%
(317) PLDs/FPGAs.....	11.18%
(318) R/F Microwave.....	5.78%
(319) Specialized Processors (GPU, NPU, etc.).....	5.98%
(320) Library Models/IP/Cores.....	13.02%
(321) Not Directly Involved in Design.....	16.38%
(322) Other.....	1.43%

4) What primary end product or service do you work on? (answer only one)

(401) Automotive Electronics.....	2.44%
(402) Avionics, Military, Government.....	3.53%
(403) Communication Systems (Wired or Wireless).....	13.49%
(404) Components (Passives).....	0.75%
(405) Computers, Systems, & Peripherals.....	7.04%
(406) Consumer Electronics.....	7.77%
(407) Medical Equipment.....	0.58%
(408) Design Services/Consultant.....	5.33%
(409) EDA Software.....	23.25%
(410) Embedded Systems.....	3.92%
(411) Research/Education.....	10.19%
(412) Semiconductors.....	15.87%
(413) Library Models/IP/Cores.....	2.59%
(414) Media/Publishing.....	3.23%
(415) Other.....	1.73%

5) Which products and/or services in the following areas do you recommend, purchase or influence the purchase of? (Answer all that apply)

(501) Analog/AMS Design/Simulation.....	20.99%
(502) Digital Logic Simulation.....	29.69%
(503) Physical Verification.....	22.27%
(504) Other Logic Verification.....	14.18%
(505) Logic Design and Synthesis.....	27.09%
(506) Packaging.....	7.05%
(507) PCB.....	7.03%
(508) Placement & Routing.....	18.74%
(509) Full-Custom Layout.....	13.86%
(510) Test.....	10.49%
(511) RTL & Formal Verification.....	23.73%
(512) Equivalence Checking.....	15.04%
(513) Design for Manufacturing.....	15.64%
(514) Algorithm Design.....	10.75%
(515) System Design and Specification.....	14.89%
(516) Software Design.....	10.92%
(517) HW/SW co-Design.....	15.66%
(518) Foundry/Silicon Vendor.....	8.03%
(519) ASSP.....	3.53%
(520) Logic/System Design.....	12.89%
(521) Physical Design.....	16.45%
(522) Verification.....	16.19%
(523) Libraries/Models.....	13.54%
(524) IP/Cores.....	17.58%
(525) None.....	21.95%

6) Which tool flow do you predominantly used? (answer only one)

(601) Custom/Structured Custom.....	28.97%
(602) COT.....	10.91%
(603) ASIC.....	41.13%
(604) Structured ASIC.....	2.40%
(605) FPGA.....	16.59%
(606) Other.....	6.81%

7) Which hotel will you be using while in Anaheim? (answer only one)

(701) Anaheim Marriott.....	18.50%
(702) Anaheim Hilton & Towers.....	22.16%
(703) Local (No need for housing).....	59.34%
(704) Other.....	22.01%

8) Is this the first time you have attended DAC?

(801) Yes.....	29.47%
(802) No.....	70.53%