

# DAC Advance Registration Form

46th DAC - July 27-31, 2009

Any registration received after June 29, 2009, will be charged the at-conference rate. If payment is received from a non-US bank, attendees will be charged a collection fee of \$45.00.

## 1 ATTENDEE INFORMATION

First Name \_\_\_\_\_ Last Name \_\_\_\_\_ Title \_\_\_\_\_

Company \_\_\_\_\_ Address \_\_\_\_\_

City \_\_\_\_\_ State/Province \_\_\_\_\_ ZIP \_\_\_\_\_ Country \_\_\_\_\_

Phone \_\_\_\_\_ Fax \_\_\_\_\_ Email (needed for receipt) \_\_\_\_\_

## 2 MEMBERSHIP STATUS

**Membership # must be included at time of submission to receive the membership rate. No refunds will be made for change in membership status.**

I certify that I am an ACM member. \_\_\_\_\_ member# \_\_\_\_\_  
signature  
 I certify that I am an IEEE member. \_\_\_\_\_ member# \_\_\_\_\_  
signature  
 I certify that I am a full time student. \_\_\_\_\_ member# \_\_\_\_\_ student# \_\_\_\_\_  
signature

This is for **individual memberships only**. This does not apply to company memberships.

## 3 TUTORIAL/WORKSHOP SELECTION

PRICES	member ACM/IEEE	non-member	ACM/IEEE student
	\$300.00	\$400.00	\$200.00

### Monday Tutorials

- 1) Low-Power SOC Design: State of the Art and Directions
- 2) High-Level Synthesis for ESL Design: Fundamentals and Case Studies

### Friday Tutorials

- 3) Post-Silicon Validation and Runtime Verification: Ensuring Correctness after First Silicon
- 4) CAD: Utilizing the State of the Art, and Beyond, in Parallel Programming
- 5) From Nanodevices to Nanosystems: Promises and Challenges of IC Design with Nanomaterials
- 6) Functional Verification Planning and Management: Navigating from Specification to Functional Closure

### Sunday Workshops

- Moving from Traditional to Equation-based DRC \$80.00 member \$110.00 non-member
- Meeting the Challenges of ESD/ERC in a Mixed-Signal World \$80.00 member \$110.00 non-member
- Multiprocessor System-on-Chip-current Trends and the Future \$150.00 member \$195.00 non-member

### Monday Workshops

- Workshop for Women in Design Automation (WWINDA): Career Crossroads - Who has the map? \$75.00 member \$100.00 non-member
- Young Faculty Workshop \$150.00 member \$195.00 non-member
- International Workshop on Bio-Design Automation \$150.00 member \$195.00 non-member

### Wednesday Workshops

- Virtual Platform Workshop at DAC \$150.00 member \$195.00 non-member

- Management Day \$95.00

### Colocated Events

- Low-Power Coalition Workshop: Advances in Low-Power Design throughout the Design Flow (Sunday) - **FREE**
- 3rd IEEE International Workshop on Design for Manufacturability and Yield 2009 (DFM&Y) (Sunday)
 

Rec'd by June 29	\$150.00member	\$200.00non-member	\$100.00student member	\$150.00student non-member
After June 29	\$200.00member	\$250.00non-member	\$130.00student member	\$200.00student non-member
- SLP 2009: International Workshop on System Level-Interconnect Prediction (Sunday)
 

Rec'd by June 29	\$250.00member	\$320.00non-member	\$200.00student member	\$250.00student non-member
After June 29	\$300.00member	\$380.00non-member	\$250.00student member	\$300.00student non-member
- Design for Manufacturability Workshop - DFM Challenges at Sub-45nm Design (Monday) - **FREE**
- 2nd IEEE International Workshop on Hardware-Oriented Security and Trust (HOST 2009) (Monday)
 

Rec'd by June 29	\$200.00member	\$250.00non-member	\$150.00student member	\$200.00student non-member
After June 29	\$250.00member	\$320.00non-member	\$200.00student member	\$250.00student non-member
- 7th IEEE Symposium on Application Specific Processors (SASP) (Monday & Tuesday)
 

Rec'd by June 29	\$315.00member	\$410.00non-member	190.00student/life/retired member
After June 29	\$420.00member	\$530.00non-member	245.00student/life/retired member
- NASA/ESA Conference on Adaptive Hardware and Systems (AHS-2009) (Wednesday-Saturday)
 

Rec'd by June 29	\$740.00member/non-member	\$590.00student
After June 29	\$870.00member/non-member	\$690.00student
- IEEE/ACM Symposium on Nanoscale Architectures (Thursday & Friday)
 

Rec'd by June 29	\$250.00member	\$330.00non-member	\$200.00student
After June 29	\$300.00member	\$380.00non-member	\$220.00student

## 4 REGISTRATION OPTIONS

### CONFERENCE FEES

	Received by June 29, 2009	After June 29, 2009 or At-Conference		Received by June 29, 2008	After June 29, 2008 or At-Conference	Conference Fees	\$ _____
Member, ACM/IEEE	\$410.00	\$500.00	<b>One-Day Only</b> (Tues/Wed/Thur)	\$260.00	\$260.00	Tutorial Fees	\$ _____
Non-member	\$510.00	\$625.00	<b>Two-Day Only</b>	\$470.00	\$470.00	User Track Fees	\$ _____
ACM/IEEE Students	\$225.00	\$285.00	(Tues/Wed, Tues/Thur, Wed/Thur) please circle day(s)			Workshop Fees	\$ _____
Exhibit-Only	\$50.00	\$95.00	<b>Workshop Only</b> (member/non-member) please select workshop			Colocated Event Fees	\$ _____
User Track member	\$175.00	\$175.00	<input type="checkbox"/> vegetarian meal			Addl. Proceedings-DVD (\$50)	\$ _____
User Track non-member	\$225.00	\$225.00				Management Day(\$95)	\$ _____
						<b>TOTAL COST</b>	\$ _____

## 5 PAYMENT INFORMATION

### Credit Cards:

- VISA
- MASTERCARD
- AMEX.

Name \_\_\_\_\_  
Please print name as it appears on the credit card

Card Number \_\_\_\_\_ Exp. Date \_\_\_\_\_ Sec. Code \_\_\_\_\_

Signature \_\_\_\_\_

**I agree to pay the total amount according to the card issuer agreement.**

**1) In which industry segment does your group or organization work?**

- \_\_\_\_\_ (101) System Design - System  
 \_\_\_\_\_ (102) System Design - Board  
 \_\_\_\_\_ (103) System Design - Hardware  
 \_\_\_\_\_ (104) System Design - Software  
 \_\_\_\_\_ (105) EDA Software  
 \_\_\_\_\_ (106) IP/Core Design  
 \_\_\_\_\_ (107) Foundry/IC Manufacturing  
 \_\_\_\_\_ (108) Design Services (Includes ASIC and Post-Silicon Services)  
 \_\_\_\_\_ (109) University/Research Institution  
 \_\_\_\_\_ (110) Financial  
 \_\_\_\_\_ (111) Publishing/Media/Industry Analyst  
 \_\_\_\_\_ (112) Other \_\_\_\_\_

**2) What is your principal job function?**

- \_\_\_\_\_ (201) Executive/Senior Management  
 \_\_\_\_\_ (202) Engineering Management  
 \_\_\_\_\_ (203) CAD Engineering Management  
 \_\_\_\_\_ (204) CAD Engineer  
 \_\_\_\_\_ (205) Hardware Logic Design Engineer  
 \_\_\_\_\_ (206) Hardware Physical Design Engineer  
 \_\_\_\_\_ (207) Verification Engineer  
 \_\_\_\_\_ (208) Application Engineer  
 \_\_\_\_\_ (209) Embedded Software Engineer  
 \_\_\_\_\_ (210) Application Software Engineer  
 \_\_\_\_\_ (211) Board Design & Layout  
 \_\_\_\_\_ (212) Test and Assembly  
 \_\_\_\_\_ (213) System Architect  
 \_\_\_\_\_ (214) Professor/Researcher  
 \_\_\_\_\_ (215) Student  
 \_\_\_\_\_ (216) Financial or Industry Analyst  
 \_\_\_\_\_ (217) Venture Capitalist  
 \_\_\_\_\_ (218) Editor/Publisher  
 \_\_\_\_\_ (219) Marketing/Sales  
 \_\_\_\_\_ (220) Other \_\_\_\_\_

**3) What type of design are you involved in?**

- \_\_\_\_\_ (301) Analog ICs/Mixed Signal ICs  
 \_\_\_\_\_ (302) Analog/Mixed Signal Systems  
 \_\_\_\_\_ (303) ASIC  
 \_\_\_\_\_ (304) Application Specific Std. Part (ASSP)  
 \_\_\_\_\_ (305) Digital ICs  
 \_\_\_\_\_ (306) Digital Systems  
 \_\_\_\_\_ (307) DSPs  
 \_\_\_\_\_ (308) RTOS

- \_\_\_\_\_ (309) Firmware  
 \_\_\_\_\_ (310) Middleware  
 \_\_\_\_\_ (311) Application  
 \_\_\_\_\_ (312) Embedded Systems  
 \_\_\_\_\_ (313) MEMS  
 \_\_\_\_\_ (314) Memory  
 \_\_\_\_\_ (315) Microproc./Microcontroller Design  
 \_\_\_\_\_ (316) PCB Layout  
 \_\_\_\_\_ (317) PLDs/FPGAs  
 \_\_\_\_\_ (318) R/F Microwave  
 \_\_\_\_\_ (319) Specialized Processors (GPU, NPU, etc.)  
 \_\_\_\_\_ (320) Library Models/IP/Cores  
 \_\_\_\_\_ (321) Not Directly Involved in Design  
 \_\_\_\_\_ (322) Other \_\_\_\_\_

**4) What primary end product or service do you work on?**

- \_\_\_\_\_ (401) Automotive Electronics  
 \_\_\_\_\_ (402) Avionics, Military, Government  
 \_\_\_\_\_ (403) Communication Systems (Wired or Wireless)  
 \_\_\_\_\_ (404) Components (Passives)  
 \_\_\_\_\_ (405) Computers, Systems, & Peripherals  
 \_\_\_\_\_ (406) Consumer Electronics  
 \_\_\_\_\_ (407) Medical Equipment  
 \_\_\_\_\_ (408) Design Services/Consultant  
 \_\_\_\_\_ (409) EDA Software  
 \_\_\_\_\_ (410) Embedded Systems  
 \_\_\_\_\_ (411) Research/Education  
 \_\_\_\_\_ (412) Semiconductors  
 \_\_\_\_\_ (413) Library Models/IP/Cores  
 \_\_\_\_\_ (414) Media/Publishing  
 \_\_\_\_\_ (415) Other \_\_\_\_\_

**5) Which products and/or services in the following areas do you recommend, purchase or influence the purchase of?**

(Answer all that apply))

**Design Tools**

- \_\_\_\_\_ (501) Analog/AMS Design/Simulation  
 \_\_\_\_\_ (502) Digital Logic Simulation  
 \_\_\_\_\_ (503) Physical Verification  
 \_\_\_\_\_ (504) Other Logic Verification  
 \_\_\_\_\_ (505) Logic Design and Synthesis  
 \_\_\_\_\_ (506) Packaging

- \_\_\_\_\_ (507) PCB  
 \_\_\_\_\_ (508) Placement & Routing  
 \_\_\_\_\_ (509) Full-Custom Layout  
 \_\_\_\_\_ (510) Test  
 \_\_\_\_\_ (511) RTL & Formal Verification  
 \_\_\_\_\_ (512) Equivalence Checking  
 \_\_\_\_\_ (513) Design for Manufacturing & Yield  
 \_\_\_\_\_ (514) Algorithm Design  
 \_\_\_\_\_ (515) System Design and Specification  
 \_\_\_\_\_ (516) Software Design  
 \_\_\_\_\_ (517) HW/SW co-Design  
 \_\_\_\_\_ (518) Foundry/Silicon Vendor  
 \_\_\_\_\_ (519) ASSP

**Services**

- \_\_\_\_\_ (520) System Logic Design  
 \_\_\_\_\_ (521) Physical Design  
 \_\_\_\_\_ (522) Verification  
 \_\_\_\_\_ (523) Post Silicon Services

**Other**

- \_\_\_\_\_ (524) Libraries/Models  
 \_\_\_\_\_ (525) IP/Cores  
 \_\_\_\_\_ (526) None

**6) Which tool flow do you predominately use?**

(Answer only one)

- \_\_\_\_\_ (601) Custom/Structured Custom  
 \_\_\_\_\_ (602) COT  
 \_\_\_\_\_ (603) ASIC  
 \_\_\_\_\_ (604) Structured ASIC  
 \_\_\_\_\_ (605) FPGA  
 \_\_\_\_\_ (606) Other \_\_\_\_\_

**7) Which hotel will you be using while in San Francisco?**

- \_\_\_\_\_ (701) San Francisco Marriott (HDQTR Hotel)  
 \_\_\_\_\_ (702) Intercontinental San Francisco  
 \_\_\_\_\_ (703) W San Francisco  
 \_\_\_\_\_ (704) Local/no need for housing  
 \_\_\_\_\_ (705) Other \_\_\_\_\_

**8) Is this the first time you have attended DAC?**

- \_\_\_\_\_ (801) Yes  
 \_\_\_\_\_ (802) No

DAC provides an email list of attendees to exhibitors. If you do not want to receive this correspondence check here. \_\_\_\_\_

**SUBMIT VIA FAX OR MAIL TO:**

Make checks payable to:  
**46th Design Automation Conference.**

**46th Design Automation Conference**

Attn: Registration Desk  
 1721 Boxelder St., Ste. 107  
 Louisville, CO 80027 USA

Fax registrations accepted with credit card payment only!

**Phone Number: (303) 530-4333**

**Toll-Free: (800) 321-4573**

**Fax Number: (303) 530-4334**

Refund Policy: Written requests for cancellations must be received on or before June 29, 2009, and are subject to a \$25.00 processing fee. Cancellations after June 29, 2009, will not be honored and all registration fees will be forfeited. No mail or fax registrations will be accepted after July 17, 2009 in the DAC office. On-line registrations will be accepted through July 31, 2009.

**TELEPHONE REGISTRATIONS ARE NOT ACCEPTED!** Fax registrations without payments will be discarded.