



DESIGN AUTOMATION CONFERENCE

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### **46th DAC Announces Power-Scavenging Topic Wins First-Ever Community-Selected Panel Vote**

LOUISVILLE, Colo. — June 30, 2009 — [The 46th Design Automation Conference \(DAC\)](#) today announced that the DAC community, through a month-long online vote, selected “Power Scavenging: Waste Not, Want Not” as its favorite topic for the first-ever [“Community-Driven” DAC Pavilion Panel](#). The new community-driven selection process is part of DAC’s ongoing efforts to ensure its program addresses the needs of IC designers and design tool users, as well as EDA developers and researchers. Hundreds cast their vote at <http://tinyurl.com/pavilionpanel> to choose among four possible topics. The four timely topics covered wearable sensor networks, power scavenging, the collision of mobile and PC technologies, and career survival advice during the current economic situation. In a very close race, 35 percent of voters selected the power scavenging topic.

With power consumption an ever-more-limiting design and system challenge, a panel of experts will discuss unexploited and hidden sources of power and how to harness them for some of today’s most complex applications. John Blyler, editor of Chip Design Magazine (Portland, Ore.), will moderate the panel. Panelists include: Professor Sandeep Kundu of the University of Massachusetts, Amherst (Amherst, Mass.); Steve Grady of Cymbet (Elk River, Minn.); and Mark Buccini of Texas Instruments (Dallas, Texas). The panel, one of 19 events in the DAC Pavilion, will be presented on Wednesday, July 29 from 10 to 10:45 a.m. in the DAC Pavilion. DAC will take place July 26 – 31, 2009 at the Moscone Center in San Francisco.

press release

“DAC is all about the design community – designers, tool users, EDA vendors, and academic researchers. It was very important to us to provide this opportunity to the design community to participate in the setting of the conference agenda, and we are heartened by the excellent response,” said Rich Goldman, chairman of the Pavilion Panel Committee. “To pay back the design community for such great support, the Panel Committee assembled an excellent panel on the topic, and I invite everyone to attend it, whether you voted for it or not.”

### **About DAC**

The Design Automation Conference (DAC) is recognized as the premier event for the design of electronic circuits and systems, and for Electronic Design Automation (EDA) and silicon solutions. A diverse worldwide community representing more than 1,000 organizations attends each year, from system designers and architects, logic and circuit designers, validation engineers, CAD managers, senior managers and executives to researchers and academicians from leading universities. Close to 60 technical sessions selected by a committee of electronic design experts offer information on recent developments and trends, management practices and new products, methodologies and technologies. A highlight of DAC is its Exhibition and Suite area with approximately 200 of the leading and emerging EDA, silicon, IP and design services providers. The conference is sponsored by the Association for Computing Machinery (ACM), the Electronic Design Automation Consortium (EDA Consortium), and the Institute of Electrical and Electronics Engineers (IEEE), and is supported by ACM's Special Interest Group on Design Automation (SIGDA) and IEEE's Council on Electronic Design Automation (CEDA), Circuits and Systems Society (CASS), and Computer-Aided Network Design (CANDE) Committee. More details are available at: [www.dac.com](http://www.dac.com).

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