



DESIGN AUTOMATION CONFERENCE

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Free Monday at DAC Includes Six Pavilion Programs

LOUISVILLE, Colo. – July 21, 2009 – The [Electronic Design Automation Consortium \(EDAC\)](#) is sponsoring the Free Monday exhibit program on Monday, July 27 at [the 46th Design Automation Conference \(DAC\)](#). In addition to the exhibition, Exhibitor Forum, IC Design Central Pavilion, and the Keynote Panel, Free Monday registration includes access to the DAC Pavilion (Booth #1928) where six Pavilion programs will be presented that day. DAC will take place July 26 – 31, 2009 at the Moscone Center in San Francisco.

“Everyone in the EDA industry should be at DAC on Free Monday and they will not want to miss the presentations in the Pavilion,” said Greg Spirakis, chair of the panels committee for the 46th DAC. “From Gary’s Smith’s annual ‘What’s Hot at DAC’ session in the morning, to discussions of system level sign-off and protecting IP, these sessions on Monday are sure to be very popular.”

The full schedule for the DAC Pavilion on Monday, July 27 is:

9:30 to 10:30 a.m. – [Gary Smith on EDA: Trends and What’s Hot at DAC](#)

10:45 to 11:45 a.m. – [The EDA Heritage Series: Doug Fairbairn’s Industry Retrospective](#)

1 to 2 p.m. – [Hogan’s Heroes: The Long Road to System-Level Sign-Off](#)

2:30 to 3:15 p.m. – [IP at Risk: Protecting the Company Jewels](#)

3:30 to 4:30 p.m. – [A Conversation with the 2009 Marie R. Pistilli Award Winner](#)

5 to 6 p.m. – [DAC/ISSCC Student Design Contest Awards Presentation](#)

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press release

The DAC Pavilion Panel program runs through Thursday, July 30 and includes 20 informative sessions that encourage audience interaction. To see the complete list of pavilion panels, visit <http://www.dac.com/events/searchevents.aspx?EventType=Pavilion%20Panel&confid=95>.

Registration

For more information, and to register for either Free Monday, Exhibits-Only or Full Conference attendance at DAC visit www.dac.com or go directly to <https://reg.mpassociates.com/reglive/register.aspx?confid=95>. All DAC registration, including Free Monday, will also be available onsite.

About DAC

The Design Automation Conference (DAC) is recognized as the premier event for the design of electronic circuits and systems, and for Electronic Design Automation (EDA) and silicon solutions. A diverse worldwide community representing more than 1,000 organizations attends each year, from system designers and architects, logic and circuit designers, validation engineers, CAD managers, senior managers and executives to researchers and academicians from leading universities. Close to 60 technical sessions selected by a committee of electronic design experts offer information on recent developments and trends, management practices and new products, methodologies and technologies. A highlight of DAC is its Exhibition and Suite area with approximately 200 of the leading and emerging EDA, silicon, IP and design services providers. The conference is sponsored by the Association for Computing Machinery (ACM), the Electronic Design Automation Consortium (EDA Consortium), and the Institute of Electrical and Electronics Engineers (IEEE), and is supported by ACM's Special Interest Group on Design Automation (SIGDA) and IEEE's Council on Electronic Design Automation (CEDA), Circuits and Systems Society (CASS), and Computer-Aided Network Design (CANDE) Committee. More details are available at: www.dac.com.

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