



DESIGN AUTOMATION CONFERENCE

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**Submissions for 2009 DAC/ISSCC Student Design Contest  
Accepted Until Dec. 8  
*Sponsors Sought for Prestigious Annual Award Program***

**BOULDER – Nov. 4, 2008** – Submissions will be accepted for the 2009 Design Automation Conference (DAC)/International Solid State Circuits Conference (ISSCC) Student Design Contest until Monday, Dec. 8, 5 p.m. Mountain time. Winners will be recognized in an award ceremony to be held during DAC, the electronic design automation (EDA) industry’s premier event, July 26-31, 2009 at the Moscone Center in San Francisco.

The prestigious annual DAC/ISSCC Student Design Contest promotes excellence in electronic systems design. It is sponsored jointly by DAC and ISSCC, and DAC sponsoring organizations and through the contributions of corporate sponsors. The competition is open to graduate and undergraduate students at universities and colleges. Winning submissions will be displayed as posters at the DAC University Booth on the exhibit floor. Selected winning entries may be included in the Technical Program, at the discretion of the Technical Program Committee. Winners will also be invited to present at a special poster session during [ISSCC 2009](#), to be held in San Francisco Feb. 8-12, 2009.

“In the electronic design community, the Student Design Contest is a well-known annual event and the best students from around the world enter each year,” said Andrew Kahng, General Chair, 46th DAC executive committee. “This contest is just one of the many ways that the DAC community serves to connect the next generation of design engineers with the industry.”

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press release

This year's Student Design Contest co-chairs are Bill Bowhill, Senior Principal Engineer, Intel Massachusetts, and Byunghoo Jung, Assistant Professor of Electrical and Computer Engineering, Purdue University.

### **Criteria for Entering, Awards and Honors**

The contest accepts designs for analog, digital or programmable circuits and systems. Submissions can be embodied as integrated circuits (ICs), reconfigurable systems, systems on chip (SoCs), platform-based or embedded system designs.

Submissions are invited from full-time graduate and undergraduate students in three categories: operational, which means that an IC design was built and tested; system design, which focuses on FPGA or other programmable architectures; and conceptual, where a project was designed and simulated, but need not have been implemented yet. The design must be part of the students' course or research work at the university and must have been completed within 18 months prior to the Dec. 8, 2008 submission deadline.

The total prize money is expected to be more than \$20,000, shared among the design award recipients. Winners will be notified prior to the 46th DAC and offered travel assistance to attend.

For more details on the Student Design Contest, visit the DAC Web site:

<http://www.dac.com/46th/studcon.html>.

### **Corporate Sponsorships Available**

Jung is actively soliciting corporate sponsor support for the contest. A sponsor company can designate a representative to serve as a contest judge, enabling the company to actively participate in the contest organization. To find out more about sponsorship opportunities, contact Byunghoo Jung at +1 (765) 494-2866 or via e-mail at [jungb@purdue.edu](mailto:jungb@purdue.edu).

### **About DAC**

The Design Automation Conference (DAC) is recognized as the premier event for the design of electronic circuits and systems, and for Electronic Design Automation (EDA) and silicon solutions. A diverse worldwide community representing more than 1,500 organizations attends each year, from system designers and architects, logic and circuit designers, validation engineers, CAD managers, senior managers and executives to researchers and academicians from leading universities. Close to 60 technical sessions selected by a committee of electronic design experts offer information on recent developments and trends, management practices and new products, methodologies and technologies. A highlight of DAC is its Exhibition and Suite area with approximately 200 of the leading and emerging EDA, silicon, design service and IP providers. The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Council on Electronic Design Automation of the Institute of Electrical and Electronics Engineers (IEEE/CASS/CEDA) and the Electronic Design Automation Consortium (EDA Consortium). More details are available at: [www.dac.com](http://www.dac.com).

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