



DESIGN AUTOMATION CONFERENCE

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**Number of Paper Submissions Up for 46th DAC**  
***Final Program to be Announced May 4***

**BOULDER, Colo. – Feb. 10, 2009** – The 46th Design Automation Conference (DAC), the electronic design automation (EDA) industry’s premier event, announced today that a total of 796 submissions were received for this year’s technical program. The 46th DAC will be held July 26 – 31, 2009 at the Moscone Center in San Francisco. The final program will be announced on May 4, 2009.

A total of 681 regular paper submissions were received, a 10 percent increase over the number received the previous year. Topic areas with notable increases in regular paper submissions for the 46th DAC compared to the 45th are: Power Analysis; Physical Design and Manufacturability; Circuit Simulation and Interconnect; Signal Integrity and Reliability; and System Level Design.

DAC also received 115 submissions for the User Track, a new addition to DAC’s technical program this year. The DAC User Track will focus on contributions by engineers using EDA tools on real designs, and will highlight best practices in the use of tool flows and methodologies. The User Track will feature up to eight sessions with approximately 30 presentations over three conference days and will provide educational and networking benefits for end-users as well as tool developers.

“This year’s strong turnout of DAC paper submissions reflects the wide-ranging work being done across industry and academia to solve emerging design issues,” said Patrick Groeneveld, Technical Program co-chair, 46th DAC Executive Committee. “Now the hard work of reviewing the papers is underway. Before the program is finalized, well over 4,000 reviews will be completed by hundreds of external reviewers, our 72-member Technical Program Committee, and our 20-member User Track Committee. We are grateful for the efforts of these distinguished volunteers. Their efforts, along with the efforts of so many researchers and practitioners worldwide who authored DAC submissions, remain the key to DAC’s leading-edge technical program.”

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press release

### **About DAC**

The Design Automation Conference (DAC) is recognized as the premier event for the design of electronic circuits and systems, and for Electronic Design Automation (EDA) and silicon solutions. A diverse worldwide community representing more than 1,500 organizations attends each year, from system designers and architects, logic and circuit designers, validation engineers, CAD managers, senior managers and executives to researchers and academicians from leading universities. Close to 60 technical sessions selected by a committee of electronic design experts offer information on recent developments and trends, management practices and new products, methodologies and technologies. A highlight of DAC is its Exhibition and Suite area with approximately 200 of the leading and emerging EDA, silicon, IP and design services providers. The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Council on Electronic Design Automation of the Institute of Electrical and Electronics Engineers (IEEE/CASS/CEDA) and the Electronic Design Automation Consortium (EDA Consortium). More details are available at: [www.dac.com](http://www.dac.com).

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