



DESIGN AUTOMATION CONFERENCE

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46th DAC Technical and Pavilion Panel Committees Developing Exciting Panel Lineup

BOULDER, Colo. — Jan. 28, 2009 — The 46th Design Automation Conference (DAC) Technical Panel Committee (TPC) and the Pavilion Panel Committee (PPC) volunteers are hard at work developing the lineup of technical and pavilion panels for the 46th DAC, which will be held July 26 – 31, 2009 at the Moscone Center in San Francisco. The committee members represent the spectrum of EDA, semiconductor and electronics companies as well as universities. They are responsible for reviewing all panel proposals and working with organizers and panelists to create informative and educational sessions on timely, compelling topics. The program for the 46th DAC, including the panel lineup, will be announced on May 4, 2009.

The Panel committee is organized into two groups, the Technical Panel committee and the Pavilion Panel Committee. This year's Panel Committee Chair, is industry veteran and consultant, Greg Spirakis, who oversees both committees.

“Each year DAC conference attendees look forward to the technical panels on the latest challenges and the lively discussions offered in the Pavilion panels on the DAC exhibit floor,” said Greg Spirakis, 46th DAC Panel Committee Chair. “This diverse group of energetic and creative volunteers is assembling panel offerings which address a wide range of design challenges and some of the most important issues facing our industry.”

This year's Technical Panel Committee members are Committee Chair Juan Rey of Mentor Graphics Corp.; Eshel Haritan, CoWare, Inc.; Ruchir Puri, IBM; Hiroyuki Yagi, Semiconductor

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Technology Academic Research Center; NS Nagaraj, Texas Instruments; Andreas Kuehlmann, Cadence Labs; and Jason Cong, University of California, Los Angeles.

This year's Pavilion Panel Committee members are Committee Chair Rich Goldman, Synopsys, Inc.; Tiffany Sparks, Chartered Semiconductor; David Lin, Denali Software; Jim Lipman, Sidense; Dave Kelf, Sigmatix; Yatin Trivedi, Synopsys, Inc. and Sabina Burns, Virage Logic.

For more information on this year's DAC Panels, contact Panel Committee Chair, Greg Spirakis at gspiraki@pacbell.net.

About DAC

The Design Automation Conference (DAC) is recognized as the premier event for the design of electronic circuits and systems, and for Electronic Design Automation (EDA) and silicon solutions. A diverse worldwide community representing more than 1,500 organizations attends each year, from system designers and architects, logic and circuit designers, validation engineers, CAD managers, senior managers and executives to researchers and academicians from leading universities. Close to 60 technical sessions selected by a committee of electronic design experts offer information on recent developments and trends, management practices and new products, methodologies and technologies. A highlight of DAC is its Exhibition and Suite area with approximately 200 of the leading and emerging EDA, silicon, IP and design services providers. The conference is sponsored by the Association for Computing Machinery's Special Interest Group on Design Automation (ACM/SIGDA), the Circuits and Systems Society and Council on Electronic Design Automation of the Institute of Electrical and Electronics Engineers (IEEE/CASS/CEDA) and the Electronic Design Automation Consortium (EDA Consortium). More details are available at: www.dac.com.

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