

Workshop Agenda. Speaker names are in italics.

	Description	Presenters
9:00	Welcome	<i>Soha Hassoun</i> , Tufts University, Medford, MA <i>Larry Lapedes</i> , Imperas, Dublin, CA
9:15-9:45	Virtual Platforms: Challenges and Opportunities	<i>Ramesh Chandra</i> , Qualcomm San Diego, CA
9:45-10:15	Modeling, Analysis And Refinement Of Heterogeneous Interconnected Systems Using Virtual Platforms	<i>Oliver Bringmann</i> , FZI Karlsruhe <i>Joachim Gerlach</i> , Robert Bosch <i>Ulrich Nageldinger</i> , Infineon Technologies <i>Jens Stellmacher</i> , Cadence Design Systems
10:15-10:45	Timing Mechanisms in Transaction Level Models	<i>Brian Bailey</i> , consultant Beaverton, OR
10:45-11:00	Break	
11:00-11:30	Assembling and Debugging VPs of Complex Multi-core Systems	<i>Tom Rathje</i> , Carbon Design Systems Acton, MA
11:30-12:00	Integration of RTL Models into Virtual Platforms via Transaction-Based Emulation	<i>Ron Choi</i> , EVE San Jose, CA
12:00-2:00	Lunch / Panel: Building and Using Virtual Platforms Moderator: <i>Michael Sanie</i> , <i>Maestro Intl.</i> , Menlo Park, CA <i>Jason Andrews</i> , Cadence, Minneapolis, MN <i>Jose Corleto</i> , Qualcomm, San Diego, CA <i>Simon Davidmann</i> , Open Virtual Platforms, Thame, United Kingdom <i>John Goodenough</i> , ARM, San Jose, CA <i>Màrius Montón</i> , GreenSocs.com, Barcelona, Spain	
2:00-2:30	Getting Started with Virtual Platforms: A Software Developer Perspective	<i>Achim Nohl</i> , CoWare Aachen, Germany
2:30-3:00	Pre-Silicon USB Driver Development on SystemC Based Virtual Platforms	<i>Filip Thoen</i> , Synopsys Mountain View, CA
3:00-3:30	Software Verification and Debug Using Virtual Platforms	<i>Jim Straus</i> , Imperas Thame, United Kingdom
3:30-3:45	Break	
3:45-4:15	Architectural/Micro-architectural Exploration on VPs	<i>Qi Zhu</i> , Michael Kishinevsky, Zhu Zhou, Atul Kwatra, Intel Corporation Hillsboro, OR
4:15-4:45	TLM Platforms Redefine Hardware Virtualization	<i>Jon McDonald</i> , Mentor Graphics, Jacksonville, FL
4:45-5:15	Software and System Verification for the MIPS Malta Platform	<i>Henry Von Bank</i> , Posedge Software Rogers, MN
5:15	Wrap up	