

# 46th DAC DEMOGRAPHICS



DESIGN AUTOMATION CONFERENCE

Conference Attendance	1962
Exhibit Only Attendees	1732
Free Monday Attendees	1605
Total Attendees	5299
Booth Staff (exhibitors)	2697
<b>Total Participants</b>	<b>7996</b>

## 46th DAC Registration Questionnaire Results

**1) In which industry segment does your group or organization work?**

101	System Design - System	9.2%
102	System Design - Board	1.2%
103	System Design - Hardware	15.8%
104	System Design - Software	3.6%
105	EDA Software	27.0%
106	IP/core design	11.7%
107	Foundry/IC Manufacturing	5.1%
108	Design Services: Includes ASIC Services & Post Silicon Services	7.5%
109	University/Research Institution	11.4%
110	Financial	1.4%
111	Publishing/Media/Industry Analyst	2.7%
112	Other	3.0%

**4) What primary end product or service do you work on?**

401	Automotive Electronics	1.9%
402	Avionics, Military, Government	1.9%
403	Communication Systems (Wired or Wireless)	10.1%
404	Components (Passives)	0.7%
405	Computer Systems & Peripherals	8.0%
406	Consumer Electronics	7.4%
407	Medical Equipment	0.7%
408	Design Services/Consultant	5.9%
409	EDA Software	25.5%
410	Embedded Systems	3.6%
411	Research/Education	9.8%
412	Semiconductors	16.0%
413	Library Models/IP/Cores	2.6%
414	Media/Publishing	2.5%
415	Other	3.3%

**2) What is your principal job function?**

201	Executive/Senior Management	16.1%
202	Engineering Management	14.6%
203	CAD Engineering Management	4.6%
204	CAD Engineer	9.3%
205	Hardware Logic Design Engineer	6.4%
206	Hardware Physical Design Engineer	6.5%
207	Verification Engineer	4.4%
208	Application Engineer	3.6%
209	Embedded Software Engineer	0.7%
210	Application Software Engineer	2.8%
211	Board Design & Layout	0.4%
212	Test and Assembly	0.5%
213	System Architect	1.6%
214	Professor/Researcher	8.7%
215	Student	7.5%
216	Financial or Industry Analyst	0.8%
217	Venture Capitalist	0.2%
218	Editor/Publisher	1.0%
219	Marketing/Sales	7.0%
220	Other	3.2%

**5) Which products and/or services in the following areas do you recommend, purchase or influence the purchase of?**

501	Analog/AMS Design/ Simulation	18.4%
502	Digital Logic Simulation	26.4%
503	Physical Verification	19.2%
504	Other Logic Verification	12.1%
505	Logic Design and Synthesis	24.5%
506	Packaging	5.9%
507	PCB	6.3%
508	Placement & Routing	18.1%
509	Full- Custom Layout	11.9%
510	Test	9.4%
511	RTL & Formal Verification	21.0%
512	Equivalence Checking	12.5%
513	Design for Manufacturing and Yield	12.9%
514	Algorithm Design	9.7%
515	System Design and Specification	12.0%
516	Software Design	11.4%
517	HW/SW co-Design	13.0%
518	Foundry/Silicon Vendor	7.3%
519	ASSP	3.1%
520	System/ Logic Design	10.9%
521	Physical Design	16.2%
522	Verification	13.3%
523	Post Silicon Services	4.6%
524	Libraries/Models	10.1%
525	IP/Cores	16.0%
526	None	2.5%

**3) What type of design are you involved in?**

301	Analog ICs/Mixed Signal ICs	21.9%
302	Analog/Mixed Signal Systems	12.4%
303	ASIC	40.8%
304	Application Specific Std. Part (ASSP)	7.4%
305	Digital ICs	31.1%
306	Digital Systems	15.7%
307	DSPs	6.7%
308	RTOS	3.0%
309	Firmware	5.7%
310	Middleware	2.6%
311	Application	8.8%
312	Embedded Systems	15.0%
313	MEMs	2.7%
314	Memory	10.0%
315	Microproc./Microcontroller Design	10.8%
316	PCB Layout	5.7%
317	PLDs/FPGAs	11.4%
318	R/F Microwave	4.0%
319	Specialized Processors (GPU, NPU, etc.)	5.9%
320	Library Models/IP/Cores	11.3%
321	Not Directly Involved in Design	16.2%

**6) Which tool flow do you predominately use?**

601	Custom/ Structured Custom	26.5%
602	COT	9.8%
603	ASIC	37.9%
604	Structured ASIC	2.3%
605	FPGA	14.1%
607	Other	9.5%