

34th DAC Awards

1997 Best Paper Award

This year, awards are made for the best papers in four categories. Winners are determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque and a cash award of \$400. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDAC (Electronic Design Automation Companies).

Physical Design and Logic Synthesis

Paper 1.1: *"An Improved Algorithm for Minimum-Area Retiming"*

Authors: **Naresh Maheshwari, Sachin Sapatnekar**

Affiliation: Iowa State Univ., Ames, IA

High-Level Synthesis, Verification and Codesign

Paper 16.2: *"Equivalence Checking Using Cuts and Heaps"*

Authors: **Andreas Kuehlmann, Florian Krohm**

Affiliation: IBM Corp., Yorktown Heights, NY

Modeling, Simulation and Estimation

Paper 17.1: *"Time-Domain and Mixed Frequency-Time algorithms for Strongly Nonlinear Circuits with Multi-Tone excitations"*

Author: **J.S. Roychowdhury**

Affiliation: Lucent Technologies, Bell Labs., Murray Hill, NJ

Design Methodology

Paper 49.1: *"Computer-Aided Design of Free-Space Opto-Electronic Systems"*

Authors: **S.P. Levitan, T.P. Kurzweg,**

M.A. Rempel, D.M. Chiarulli

Affiliation: Univ. of Pittsburgh, Pittsburgh, PA

P.J. Marchand, C. Fan,

F.B. McCormick

Univ. of California, La Jolla, CA

Advancement in Computer Science and Electrical Engineering Undergraduate Scholarships

The objective of the ACSEE Scholarship program is to increase the pool of professionals in Electrical Engineering and Computer Science from under-represented groups (Women, African American, Hispanic, Native American, and Physically Challenged). In 1989, ACM Special Interest Group on Design Automation (SIGDA) began providing the program. Beginning in 1993, the Design Automation Conference provides the funds for the scholarship and SIGDA continues to administer the program for DAC. DAC funds two \$4000 scholarships renewable up to 5 years to graduating high school seniors. The former International Daisy User Group funds one \$1000 one-time-only scholarship. The 1997 winners will be announced at the Conference. The 1996 winners were:

1996 DAC/IDUG ACSEE Undergraduate Scholarships

DAC \$4K: **Ann Nuñez Alejandro**, Houston, TX - attending University of Houston

DAC \$4K: **Ellen Weiss Carvill**, Milford, NY - attending Carleton College

IDUG \$1K: **Shawn P. Koch**, Houston, TX - attending Rice University

This year, the graduates of the ACSEE program include **Hector Villalobos** who will be receiving his BS in Electrical Engineering from Santa Clara University and **Sung Jo** who will receive his BS in Electrical Engineering from San Jose State University.

For more information about the ACSEE scholarship, please contact Dr. Cherrice Traver, EE/CS Department, Union College, Schenectady, NY 12308 email: traverc@doc.union.edu.

Design Automation Conference Graduate Scholarships

Each year the Design Automation Conference sponsors several \$12,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems". Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Information on next year's DAC scholarship award program will be available on the DAC World Wide Web page at: <http://www.dac.com/scholarship.html>.

Design Automation Conference Graduate Scholarship Awards

- Prof. **R. Iris Bahar** of the Brown University, Providence, RI, for **Qian Cui**. Their project is entitled, "*Using Implications to Drive Low-Power Optimization of Technology-Dependent Circuits*".
- Prof. **David Kaeli** of the Northeastern University, Boston, MA, for **Ying Liu**. Their project is entitled, "*Designing in the 3rd Dimension*".
- Prof. **Miodrag Potkonjak** of the University of California, Los Angeles, CA, for **Inki Hong**. Their project is entitled, "*Design Methodology for Synthesis of Real-Time Systems on Silicon*".
- Prof. **Shambhu J. Upadhyaya** of the State University of New York, Buffalo, NY, for **Kamran Zarrineh**. Their project is entitled, "*A Design for Test Perspective on Memory Synthesis*".

The Lucent Technologies supported DAC Graduate Scholarship is awarded to:

- Prof. **Sachin S. Sapatnekar** of the Iowa State University, Ames, IA, for **Naresh maheshwari** and **Yanbin Jiang**. Their project is entitled, "*Practical Methods for Gate-Level and Transistor-Level Timing Optimization*".

The Xilinx supported DAC Graduate Scholarship is awarded to:

- Prof. **Michael J. Alexander** of the Washington State University, Pullman, WA, for **Yongmin Ge**. Their project is entitled, "*CAD-Driven FPGA Architecture Design*".

Design Automation Conference Graduate Scholarship Committee

The 1997 DAC Scholarship Committee was comprised of the following people:

James P. Cohoon - University of Virginia (Chair)

Philip V. Lopresti - Independent Consultant

Jeffrey S. Salowe - Cadence Design Systems, Inc.

1997 SIGDA Meritorious Service Award

Robert A. Walker - Kent State University, Kent, OH