

34th DAC (1997) Best Paper Award

This year, awards are made for the best papers in four categories. Winners are determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque and a cash award of \$400. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDAC (Electronic Design Automation Companies).

Physical Design and Logic Synthesis

Paper 1.1: "An Improved Algorithm for Minimum-Area Retiming"

Authors: Naresh Maheshwari, Sachin Sapatnekar - *Iowa State Univ., Ames, IA*

High-Level Synthesis, Verification and Codesign

Paper 16.2: "Equivalence Checking Using Cuts and Heaps"

Authors: Andreas Kuehlmann, Florian Krohm - *IBM Corp., Yorktown Heights, NY*

Modeling, Simulation and Estimation

Paper 17.1: "Time-Domain and Mixed Frequency-Time algorithms for Strongly Nonlinear Circuits with Multi-Tone excitations"

Author: J.S. Roychowdhury - *Lucent Technologies, Bell Labs, Murray Hill, NJ*

Design Methodology

Paper 49.1: "Computer-Aided Design of Free-Space Opto-Electronic Systems"

Authors: S.P. Levitan, T.P. Kurzweg, P.J. Marchand, C. Fan - *Univ. of Pittsburgh, Pittsburgh, PA*

M.A. Rempel, D.M. Chiarulli F.B. McCormick - *Univ. of California, La Jolla, CA*