

35th DAC Awards

1998 Best Paper Award

This year, awards are made for the best papers in four categories. Winners are determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque and a cash award of \$400. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDA Consortium (Electronic Design Automation Companies).

Physical Design and Logic Synthesis

Paper 17.1: "*Generic Global Placement and Floorplanning*"

Authors: **Hans Eisenmann, Frank M. Johannes**

Affiliation: Technical Univ. of Munich, Munich, Germany

High-Level Synthesis, Verification and Codesign

Paper 32.1: "*A Decision Procedure for Bit-Vector Arithmetic*"

Authors: **Clark W. Barrett, David L. Dill, Jeremy R. Levitt**

Affiliation: Stanford Univ., Stanford, CA

Modeling Simulation and Estimation

Paper 13.1: "*A Fast Hierarchical Algorithm For 3-D Capacitance Extraction*"

Authors: **Weiping Shi, Jianguo Liu, Naveen Kakani** **Tiejun Yu**

Affiliation: Univ. of North Texas, Denton, TX Univ. of North Carolina, Charlotte, NC

Design Methodology

Paper 9.1: "*OCCOM: Efficient Computation of Observability-Based Code Coverage Metrics for Functional Verification*"

Authors: **Farzan Fallah, Srinivas Devadas** **Kurt Keutzer**

Affiliation: Massachusetts Inst. of Tech., Cambridge, MA Synopsys, Inc., Mountain View, CA

Paper 39.1: "*Functional Verification of a Multiple-Issue, Out-of-Order, Superscaler Alpha Processor—The alpha 21264 CPU Chip*"

Authors: **Michael Quinn, Scott Taylor**

Affiliation: Digital Equipment Corp., Hudson, MA

Advancement in Computer Science and Electrical Engineering Undergraduate Scholarships

The objective of the ACSEE Scholarship program is to increase the pool of professionals in Electrical Engineering and Computer Science from under-represented groups (Women, African American, Hispanic, Native American, and Physically Challenged). In 1989, ACM Special Interest Group on Design Automation (SIGDA) began providing the program. Beginning in 1993, the Design Automation Conference provides the funds for the scholarship and SIGDA continues to administer the program for DAC. DAC normally funds two \$4000 scholarships renewable up to 5 years to graduating high school seniors. In 1997 three awards were made. The 1998 winners will be announced at the Conference.

1997 DAC ACSEE Undergraduate Scholarships

DAC \$4K: **Bryan Neil Ramirez**, Crook, CO - attending Colorado School of Mines

DAC \$4K: **Christine Joyce Mina**, Minot, MA - attending Worcester Polytechnic Institute

DAC \$4K: **Diala William Abboud**, Charlotte, NC - attending Univ. of North Carolina at Charlotte

For more information about the ACSEE scholarship, please contact Dr. Cherrice Traver, EE/CS Department, Union College, Schenectady, NY 12308 email: traverc@doc.union.edu.

Design Automation Conference Graduate Scholarships

Each year the Design Automation Conference sponsors several \$24,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems". Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Information on next year's DAC scholarship award program will be available on the DAC World Wide Web page at: <http://www.dac.com/scholarship.html>.

Design Automation Conference Graduate Scholarship Awards

- Prof. **Rajesh K. Gupta** of the University of California, Irvine, CA, for **Ali Dasdan**. Their project is entitled, "*Framework for Timing Constraint Analysis and Debugging for Embedded Systems*".
- Prof. **John Lillis** of the University of Illinois, Chicago, IL, for **Sung-Woo Hur** and **Prashanthi Malireddy**. Their project is entitled, "*New Techniques for Timing-Driven Placement*".
- Prof. **Frank Vahid** of the University of California, Riverside, CA, for **Tony Givargis**. Their project is entitled, "*Interface Exploration for Core-Based Systems*".

The IEEE CAS supported DAC Graduate Scholarship is awarded to:

- Prof. **Soha Hassoun** of the Tufts University, Medford, MA, for **Alexndros Margomenos**, **Terry Orfanos** and **Edmund Sullivan**. Their project is entitled, "*Microarchitectural Optimizations and Synthesis*".

Design Automation Conference Graduate Scholarship Committee

The 1998 DAC Scholarship Committee was comprised of the following people:

- James P. Cohoon** - University of Virginia (Chair)
- Michael Lightner** - University of Colorado
- Jeffrey S. Salowe** - Cadence Design Systems, Inc.

1998 SIGDA Meritorious Service Awards

For contributions in producing SIGDA CD ROM's- Archiving the knowledge of the Design Automation Community

- Jason Cong** - University of California, Los Angeles, CA
- Bryan Preas** - Xerox PARC, Palo Alto, CA
- Kathy Preas** - KP Publications on CD-ROM, Palo Alto, CA
- Chong-Chian Koh** - University of California, Los Angeles, CA
- Cheng-Kok Koh** - University of California, Los Angeles, CA

1998 IEEE Fellows

- William Joyner** - IBM Corp., T.J. Watson Research Ctr., Yorktown Heights, NY
- Michael Lightner** - Univ. of Colorado, Boulder, CO
- Michael Nakhala** - Carleton Univ., Ottawa, ON, Canada
- Rob A. Rutenbar** - Carnegie Mellon Univ., Pittsburgh, PA
- Karem Sakallah** - Univ. of Michigan, Ann Arbor, MI