

# 35th DAC (1998) Best Paper Award

This year, awards are made for the best papers in four categories. Winners are determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque and a cash award of \$400. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDA Consortium (Electronic Design Automation Companies).

## **Physical Design and Logic Synthesis**

### **Paper 17.1: “Generic Global Placement and Floorplanning”**

Authors: Hans Eisenmann, Frank M. Johannes

Affiliation: *Technical Univ. of Munich, Munich, Germany*

## **High-Level Synthesis, Verification and Codesign**

### **Paper 32.1: “A Decision Procedure for Bit-Vector Arithmetic”**

Authors: Clark W. Barrett, David L. Dill, Jeremy R. Levitt

Affiliation: *Stanford Univ., Stanford, CA*

## **Modeling Simulation and Estimation**

### **Paper 13.1: “A Fast Hierarchical Algorithm For 3-D Capacitance Extraction”**

Authors: Weiping Shi, Jianguo Liu, Naveen Kakani Tiejun Yu

Affiliation: *Univ. of North Texas, Denton, TX Univ. of North Carolina, Charlotte, NC*

## **Design Methodology**

### **Paper 9.1: “OCCOM: Efficient Computation of Observability-Based Code Coverage Metrics for Functional Verification”**

Authors: Farzan Fallah, Srinivas Devadas Kurt Keutzer

Affiliation: *Massachusetts Inst. of Tech., Cambridge, MA Synopsys, Inc., Mountain View, CA*

### **Paper 39.1: “Functional Verification of a Multiple-Issue, Out-of-Order, Superscaler Alpha Processor—The alpha 21264 CPU Chip”**

Authors: Michael Quinn, Scott Taylor

Affiliation: *Digital Equipment Corp., Hudson, MA*