

37th DAC Awards

2000 IEEE Fellows

- **Raul Camposano** - Synopsys, Inc., Mountain View, CA
For contributions to behavioral synthesis of integrated circuits and systems
- **Chung-Kuan Cheng** - University of California, Santa Barbara, CA
For contributions to circuit partitioning and physical layout automation
- **Kwang-Ting Cheng** - University of California, Santa Barbara, CA
For contributions to innovative techniques for testing and synthesis of electronic circuits
- **Manfred Glesner** - Darmstadt University of Technology, Darmstadt, Germany
For contributions to the development of microelectronic system design and education in microelectronics

CAD Transactions Best Paper Award

Chris C.N. Chu and **Martin D.F. Wong** - Univ. of Texas, Austin, TX

For the paper: "A Quadratic Programming Approach to Simultaneous Buffer Insertion/Sizing and Wire Sizing" IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 18, no. 6, pp. 787-798, June 1999.

VLSI Transactions Best Paper Award

- **Daniel D. Gajski** - Univ. of California, Irvine, CA
- **Frank Vahid** - Univ. of California, Riverside, CA
- **Sanjiv Narayan** - Cadence Design Systems, Inc., Santa Clara, CA
- **Jie Gong** - Qualcomm Inc., San Diego, CA

For the paper: "SpecSyn: An Environment Supporting the Specify-Explore-Refine Paradigm for Hardware/Software System Design," IEEE Transactions on VLSI Systems, vol. 6, no. 1, pp. 84-100, March 1998.

Outstanding Young Author Award

- **Amir H. Salek** and **Jinan Lou** - Univ. of Southern California, Los Angeles, CA

For the paper: "An Integrated Logical and Physical Design Flow for Deep Submicron Circuits" IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 18, no. 9, pp. 1305-1315, September 1999.

2000 Best Paper Award

This year, awards are made for the best papers in four categories. Winners were determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDA Consortium (Electronic Design Automation Consortium).

Design Synthesis, Test and Validation

Paper 2.1: "To Split or to Conjoin: The Question in Image Computation"
Authors: **In-Ho Moon, Fabio Somenzi** and **James Kukula**
Affiliation: Univ. of Colorado, Boulder, CO and Synopsys, Inc., Beaverton, OR
Author: **Kavita Ravi**
Affiliation: Cadence Design Systems, Inc., New Providence, NJ

Analog/RF/Electrical Modeling and Simulation

Paper 11.1: "A Multi-Interval Chebyshev Collocation Method for Efficient High-Accuracy RF Circuit Simulation"
Authors: **Baolin Yang, Joel R. Phillips**
Affiliation: Cadence Design Systems, Inc., San Jose, CA

Design Methodology

Paper 4.1: "On-Chip Inductance Modeling and Analysis"
Author: **Kaushik Gata, Vladimir Zolotov, Rajendran V. Panda, Brian Young, David Blaauw**
Affiliation: Motorola, Inc., Austin, TX

Embedded Systems

Paper 29.3: "Communication Architecture Tuners: A Methodology for the Design of High performance Communication Architectures for System-on-Chips"
Authors: **Kanishka Lahiri, Sujit Dey, Anand Raghunathan, Lakshminarayana Ganesh**
Affiliation: Univ. of California at San Diego, La Jolla, CA

The following people have requested to receive their medal at the 37th Design Automation Conference

IEEE Millennium Medals (In celebration of the third millennium)

- **Robert K. Brayton** - Univ. of California, Berkeley, CA
- **Robert W. Brodersen** - Univ. of California, Berkeley, CA
- **Stephen Director** - Univ. of Michigan, Ann Arbor, MI
- **Alfred E. Dunlop** - Lucent Technologies-Bell Labs., Murray Hill, NJ
- **Chung Luang (Dave) Liu** - National Tsing Hua Univ., Taiwan, ROC
- **Sanjit Mitra** - Univ. of California, Santa Barbara, CA
- **Ronald A. Rohrer** - Intersouth Partners, Research Triangle Park, NC

IEEE CAS Golden Jubilee Medals (For outstanding contributions to IEEE Circuits and Systems Society)

- **Robert K. Brayton** - Univ. of California, Berkeley, CA
- **Robert W. Broderson** - Univ. of California, Berkeley, CA
- **Randal E. Bryant** - Carnegie Mellon University, Pittsburgh, PA
- **Ralph Cavin, III** - Semiconductor Research Corp., Research Triangle Park, NC
- **Hugo De Man** - Katholieke Univ., Leuven, Belgium
- **Giovanni De Micheli** - Stanford University, Stanford, CA
- **Stephen Director** - Univ. of Michigan, Ann Arbor, MI
- **Michael Lightner** - Univ. of Colorado, Boulder, CO
- **Chung Luang (Dave) Liu** - National Tsing Hua Univ., Taiwan, ROC
- **Sanjit Mitra** - Univ. of California, Santa Barbara, CA
- **A. Richard Newton** - Univ. of California, Berkeley, CA
- **Donald Pederson** - Univ. of California, Berkeley, CA
- **Fazlollah Reza** - Concordia Univ., Montreal, Canada
- **Ronald A. Rohrer** - Intersouth Partners, Research Triangle Park, NC
- **Alberto L. Sangiovanni-Vincentelli** - Univ. of California, Berkeley, CA
- **Andrzej Strojwas** - Carnegie Mellon Univ., Pittsburgh, PA
- **Louis Weinberg** - City Univ. of New York, New York, NY
- **Ping Yang** - Taiwan Semiconductor Manufacturing Co. Ltd., Taiwan, ROC

Advancement in Computer Science and Electrical Engineering Undergraduate Scholarships

The objective of the ACSEE Scholarship program is to increase the pool of professionals in Electrical Engineering and Computer Science from under-represented groups (Women, African American, Hispanic, Native American, and Physically Challenged). In 1989, ACM Special Interest Group on Design Automation (SIGDA) began the program. Beginning in 1993, the Design Automation Conference provides the funds for the scholarship and SIGDA continues to administer the program for DAC. DAC normally funds two or three \$4000 scholarships, renewable up to 5 years to graduating high school seniors. In 1999 the IEEE Circuits and Systems Society also began to sponsored one scholarship. The 2000 winners will be announced at the Conference. The 1999 winners were:

1999 ACSEE Undergraduate Scholarships

- DAC \$4K: **Awrasa Monique Monterichok**, Los Angeles, CA - attending UCLA
- DAC \$4K: **Mohamed Ahmed Elton**, Hixson, TN - attending Duke University
- DAC \$4K: **Anna Alexandra Dreyer**, Owings Mills, MD - attending MIT
- DAC \$4K: **Karl Huckel**, Largo, FL, - attending Carnegie Mellon University

For more information about the ACSEE scholarship, please contact Dr. Cherrice Traver, EE/CS Department, Union College, Schenectady, NY 12308 email: traverc@doc.union.edu.

Design Automation Conference Graduate Scholarships

Each year the Design Automation Conference sponsors several \$24,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems". Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Information on next year's DAC scholarship award program will be available on the DAC World Wide Web page at: <http://www.dac.com/scholarship.html>.

ACM Outstanding Ph.D. Dissertation Award in Electronic Design

- Robert Brent Jones** - Stanford Univ., Stanford, CA, for:
Applications of Symbolic Simulation to Formal Verification of Microprocessors