

37th DAC (2000) Best Paper Award

This year, awards are made for the best papers in four categories. Winners were determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDA Consortium (Electronic Design Automation Consortium).

Design Synthesis, Test and Validation

Paper 2.1: “To Split or to Conjoin: The Question in Image Computation”

Authors: In-Ho Moon, Fabio Somenzi James Kukula

Affiliation: *Univ. of Colorado, Boulder, CO Synopsys, Inc., Beaverton, OR*

Author: Kavita Ravi

Affiliation: *Cadence Design Systems, Inc., New Providence, NJ*

Analog/RF/Electrical Modeling and Simulation

Paper 11.1: “A Multi-Interval Chebyshev Collocation Method for Efficient High-Accuracy RF Circuit Simulation”

Authors: Baolin Yang, Joel R. Phillips

Affiliation: *Cadence Design Systems, Inc., San Jose, CA*

Design Methodology

Paper 4.1: “On-Chip Inductance Modeling and Analysis”

Author: Kaushik Gata, Vladimir Zolotov, Rajendran V. Panda, Brian Young, David Blaauw

Affiliation: *Motorola, Inc., Austin, TX*

Embedded Systems

Paper 29.3: “Communication Architecture Tuners: A Methodology for the Design of High performance Communication Architectures for System-on-Chips”

Authors: Kaniishka Lahiirrii,, Sujjiitt DeyAnand Raghunatthan,, Lakshmiinarrayana Ganesh

Affiliation: *Univ. of California at San Diego, La Jolla, CA*