

# 33rd DAC Awards

## 1996 Best Paper Award

This year, awards are made for the best papers in five categories. Winners are determined from detailed reviews of the accepted papers in the technical sessions. Each award is accompanied by a plaque and a cash award of \$400. The awards are given by ACM/SIGDA (Special Interest Group on Design Automation), IEEE/CAS (Institute of Electrical and Electronics Engineers/Circuits and Systems Society) and EDAC (Electronic Design Automation Companies).

Physical Design, Electrical Simulation, High Speed and Analog Design

Paper 6.4: "*A Probability-Based Approach to VLSI Circuit Partitioning*"

Authors: Shantanu Dutt, Wenyong Deng

Affiliation: Univ. of Minnesota, Minneapolis, MN

High -Level and System Synthesis and Frameworks

Paper 39.1: "*Using Register-Transfer Paths in Code Generation for Heterogeneous Memory-Register Architectures*"

Authors: Guido Araujo, Sharad Malik Mike Tien-Chien Lee

Affiliations: Princeton Univ., Princeton, NJ Fujitsu Labs. of America, Santa Clara, CA

Logic Synthesis, Verification and Test

Paper 45.4: "*On Solving Binat Covering Problems*"

Author: Olivier Coudert

Affiliation: Synopsys, Inc., Mountain View, CA

Design Methodology

Paper 3.1 "*HEAT: Hierarchical Energy Analysis Tool*"

Authors: J. Satyanarayana, Keshab K. Parhi

Affiliations: Univ. of Minnesota, Minneapolis, MN

Design Experiences and Management of DA Systems

Paper 3.3: "*POSE: Power Optimization and Synthesis Environment*"

Authors: Sasan Iman, Massoud Pedram

Affiliation: Univ. of Southern California, Los Angeles, CA

## 1995 SIGDA Meritorious Service Awards

Steve Levitan – Univ. of Pittsburgh, PA

## 1996 ACM Fellows

The ACM Fellow Program recognizes and honors outstanding ACM members for their achievements in computer science and information technology and for their significant contributions to the mission of ACM. Among the ACM Fellows of 1996 VLSI systems.

Mary Jane Irwin – Pennsylvania State Univ., University Park, PA

Bryan Preas – Xerox Parc, Palo Alto, CA

Sartaj Sahni – Univ. of Florida, Gainesville, FL

## 1996 IEEE Fellows

The grade of Fellow recognizes unusual distinction in the profession and shall be conferred only by invitation of the Board of Directors upon a person of outstanding and extraordinary qualifications and experience in IEEE designated fields, who has made important individual contributions to one or more of these fields.

Tushar Gheewala – Crosscheck Technology, Los Altos, CA

Kurt Keutzer – Synopsys, Inc., Mountain View, CA

Majid Sarrafzadeh – Northwestern Univ., Evanston, IL

## 1996 Design Automation Conference Scholarship Awards

The 1996 DAC Scholarship Committee was comprised of the following people:

Sylvia A. Chanak - Cadence Design Systems, Inc. (Chair)

James P. Cohoon - Univ. of Virginia

Philip V. Lopresti - Independent Consultant

### **The 1996 Design Automation Conference Scholarship Awards**

- Prof. Srinivasa Vemuru of The City University of New York, for Ali Duale. Their project is entitled "*High-Level Test Generation*".
- Prof. Peichen Pan of the Clarkson University for Arvind Karandikar. Their project is entitled "*Clustering for Sequential Circuits*".
- Prof. Frank Vahid of The University of California, Riverside for Linus Tauro and Thuy Le. Their project is entitled "*Hardware/Software Partitioning and Communication*".
- Prof. Adnan Aziz of The University of Texas at Austin for Khurram Sajid. Their project is entitled "*Formal Design Verification and Sequential Synthesis*".

### **Advancement in Computer Science and Electrical Engineering Undergraduate Scholarships**

The objective of the ACSEE Scholarship program is to increase the pool of professionals in Electrical Engineering and Computer Science from under-represented groups (Women, African America, Hispanic, Native American, and Physically Challenged). In 1989, ACM Special Interest Group on Design Automation (SIGDA) began providing the program. Beginning in 1993, the Design Automation Conference provides the funds for the scholarship and SIGDA continues to administer the program for DAC. DAC funds two \$4000 scholarships renewable up to 5 years to graduating high school seniors. The former International Daisy User Group funds one \$1000 one-time-only scholarship. The 1996 winners will be announced at the Conference. The 1995 winners were:

#### **1995 DAC/IDUG ACSEE Undergraduate Scholarships**

DAC \$4K: Amy Wai-Yee Ng, Sugarland, TX – attending MIT

DAC \$4K: Micah Tremayne Hawthorne, Ocean Springs, MS – attending MIT

IDUG \$1K: Helen Bui, Pasadena, MD

This year, the first graduates of the ACSEE scholarship program will be attending DAC. They are Ms. Ender Bilir from Princeton who graduated with a degree in Computer Science, Ms. Tessa Wilbert from the University of California at Davis who graduated with a double major in Computer Science and Electrical Engineering and Mr. Alonzo Castro from MIT who graduated with a degree in Electrical Engineering.

For more information about ACSEE, please contact Dr. Cherrice Traver, EE/CS Department, Union College, Schenectady, NY 12308. email: [traverc@doc.union.edu](mailto:traverc@doc.union.edu)

### **Design Automation Conference Scholarship Awards**

Each year the Design Automation Conference sponsors several \$12,000 scholarships to support graduate research and study in Design Automation (DA), with emphasis in "design and test automation of electronic and computer systems." Each scholarship is awarded directly to a university for the Faculty Investigator to expend in direct support of one or more DA graduate students.

The criteria for granting such a scholarship expanded in 1996 to include financial need. The criteria are: the academic credentials of the student(s); the quality and applicability of the proposed research; the impact of the award on the DA program at the institution; and financial need. Preference is given to institutions that are trying to establish new DA research programs.

Information on next year's DAC scholarship award program will be available on the DAC World Wide Web page at: <http://www.dac.com/dac/scholarship.html>

#### **1995 Design Automation Conference Scholarship Awards**

The 1995 DAC Scholarship Committee was comprised of the following people:

Herschel H. Loomis, Naval Postgraduate School (Chair)

Sylvia A. Chanak, Cadence Design Systems, Inc.

Ian Getreu, Analogy, Inc.

#### **1995 Design Automation Conference Scholarship Awards**

- Dr. Jay Brockman of the University of Notre Dame for Arumugamangala Lokanathan and Eric Johnson. Their project is entitled "*Multidisciplinary Design Methodologies for Integrated Circuits*".
- Professor Malgorzata Chrzanowska-Jeske of Portland State University for Zhi Wang and Jinson Zhang. Their project is entitled "*Fine-Grain Locally-Connected FPGAs; Synthesis and Architecture*".
- Professor Andrew Kahng of University of California, Los Angeles for Charles Alpert and Chung-Wen Albert Tsao. Their projects are entitled "*New Approaches to Partitioning and Clustering*", and "*New Methods for Clock Distribution*".
- Professor Alex Orailoglu of University of California, San Diego for Ian Harris. Their project is entitled "*Synthesis of Self-Testable VLSI Designs*".
- Professor Majid Sarrafzadeh of Northwestern University for Salil Raje and Amir H. Farrahi. Their project is entitled "*Behavioral Power Estimation and Optimization*", and is a renewal of a 1994 DAC Scholarship.